Energy Efficient Innovations

www.onsemi.com



ON Semiconductor[®]

Importing Spice Models into OrCAD

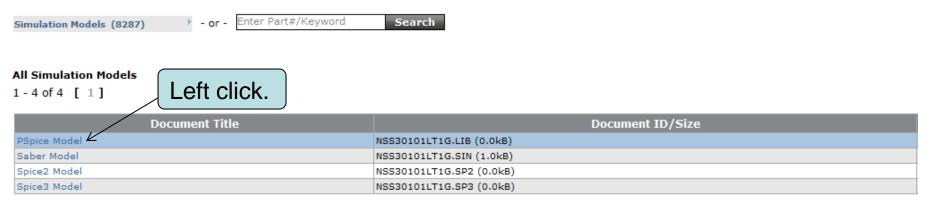
Download .LIB File from ONSemi.com

Home > Support > Design Support > Design Resources > Simulation Models

Simulation Models

A complete listing of models for ON Semiconductor products. ON Semiconductor provides many model types including SPICE, PSPICE, ISPICE and ORCAD.

Search for models by using the flyout menus or by entering a part number or keyword. Use table column headers to sort the columns.





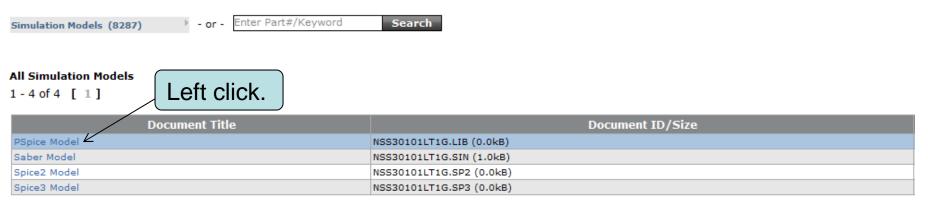
Download .LIB File from ONSemi.com

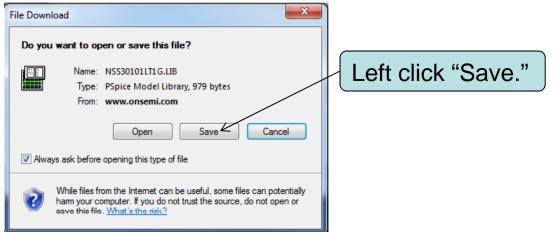
Home > Support > Design Support > Design Resources > Simulation Models

Simulation Models

A complete listing of models for ON Semiconductor products. ON Semiconductor provides many model types including SPICE, PSPICE, ISPICE and ORCAD.

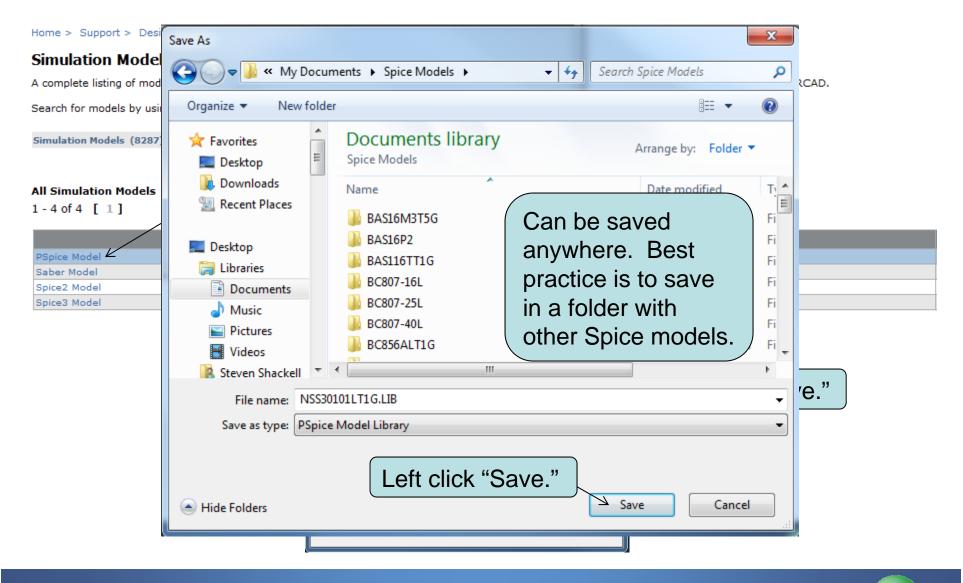
Search for models by using the flyout menus or by entering a part number or keyword. Use table column headers to sort the columns.







Download .LIB File from ONSemi.com



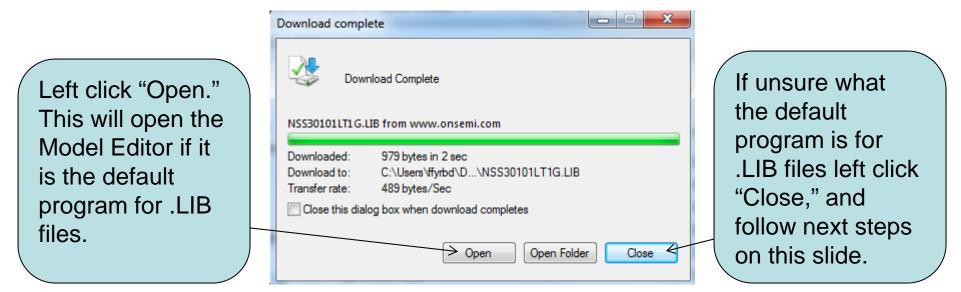
4 • TND6045/D • Apr-13

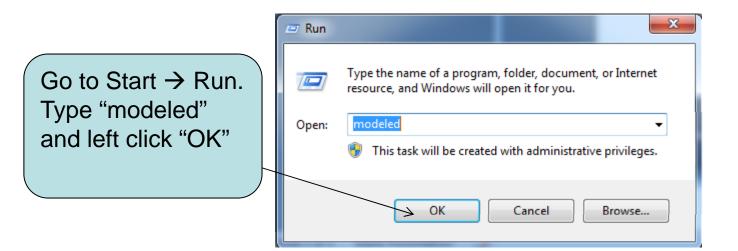
ON

Left click "Open." This will open the Model Editor if it is the default program for .LIB files.

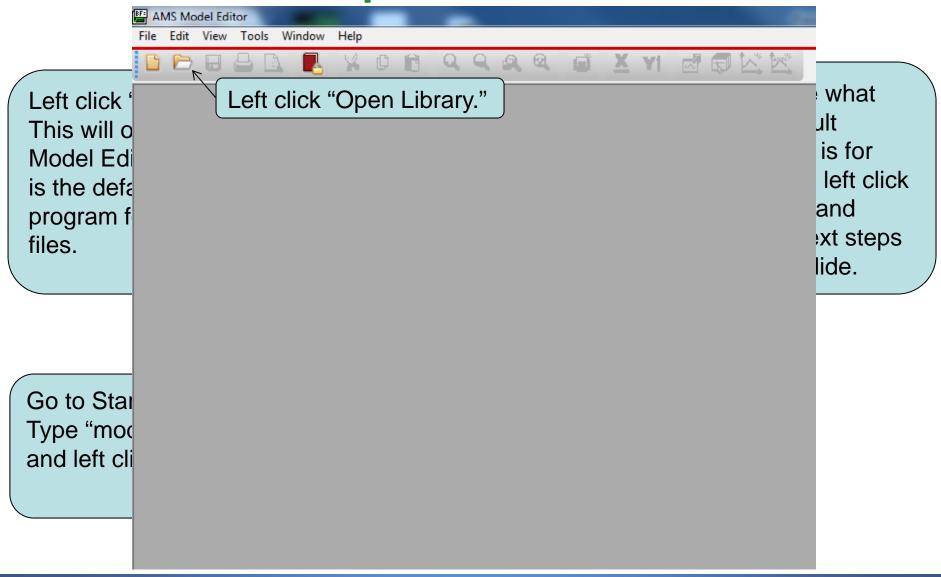
Download complete	
Download Complete	If unsure what the default
	program is for
Downloaded: 979 bytes in 2 sec Download to: C:\Users\ffyrbd\D\NSS30101LT1G.LIB	.LIB files left click
Transfer rate: 489 bytes/Sec	"Close," and
Close this dialog box when download completes	follow next steps
> Open Open Folder Close	on this slide.



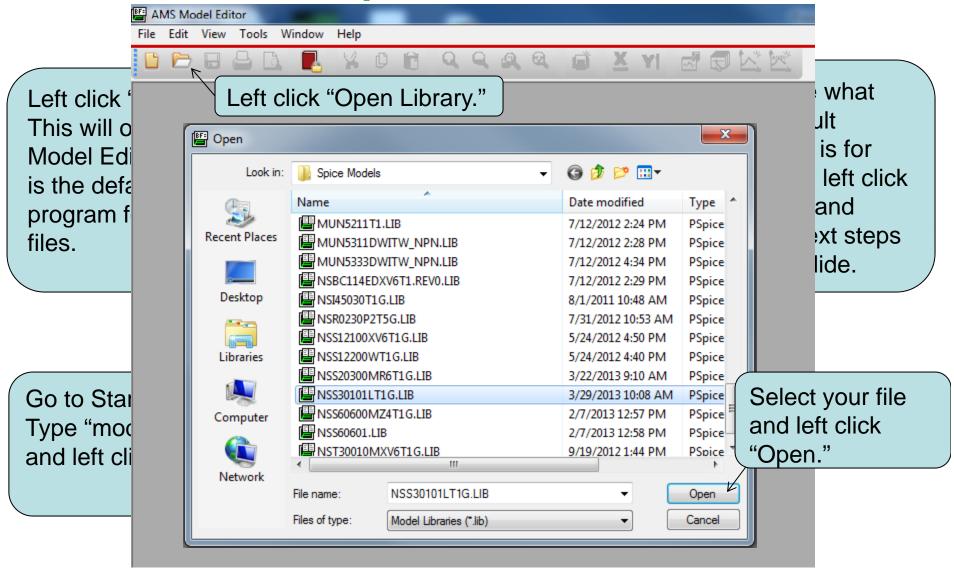




ON

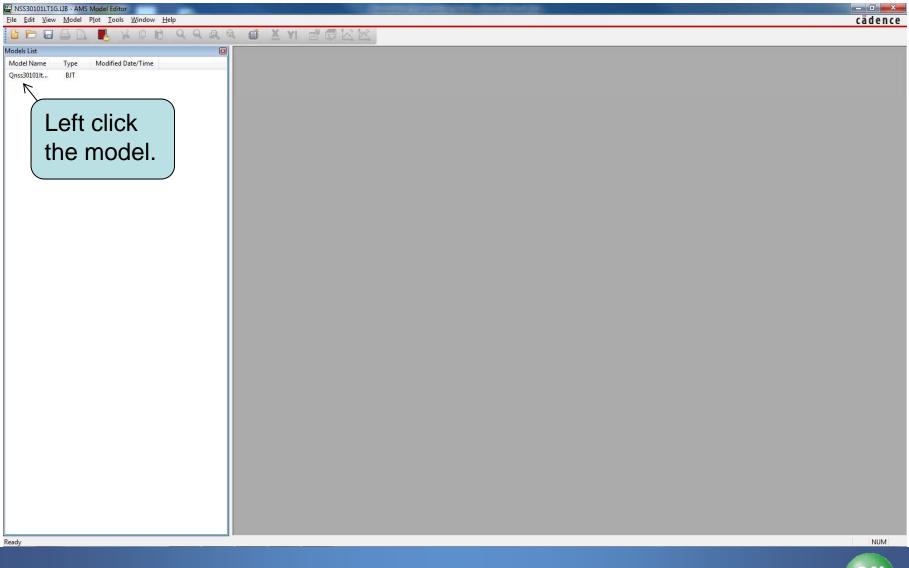






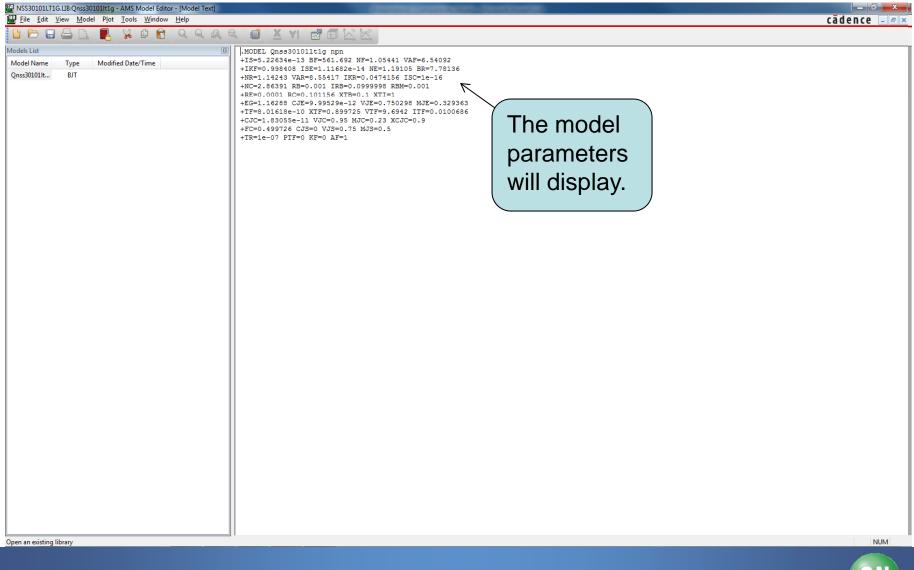


Display Parameters



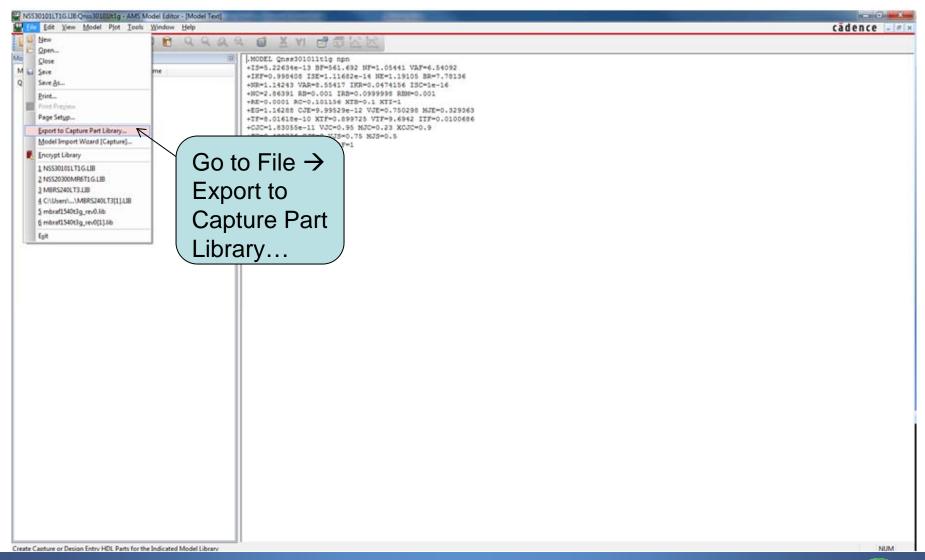


Display Parameters



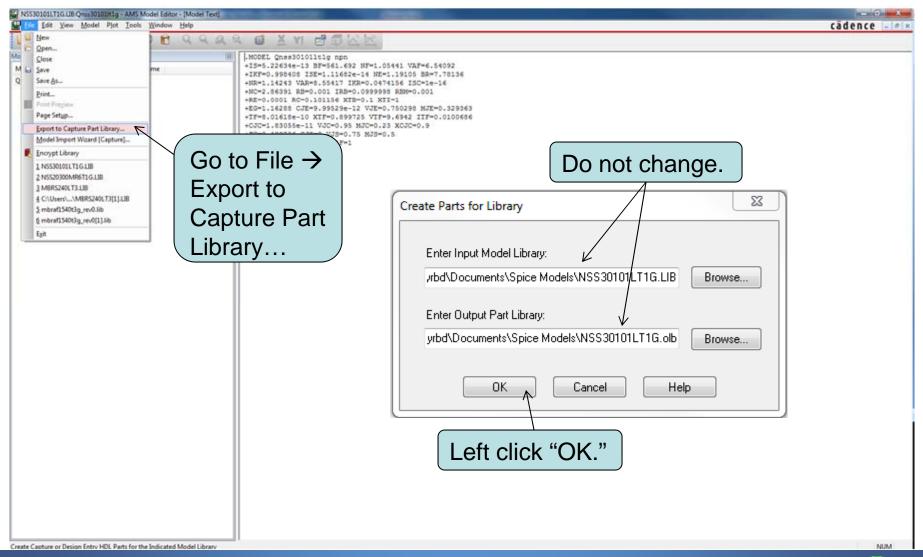


Export to .OLB File



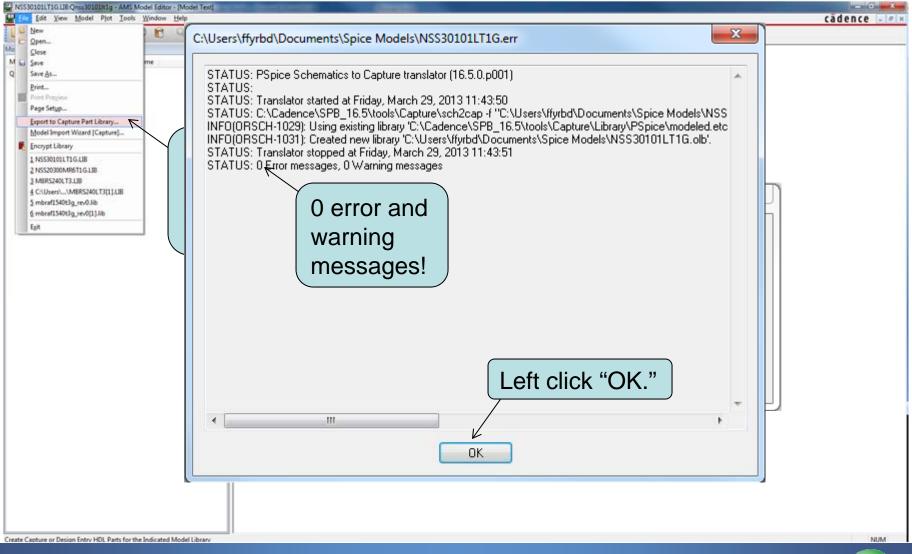


Export to .OLB File



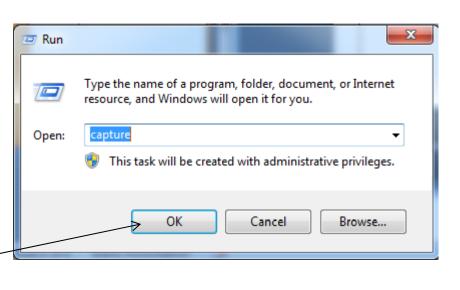


Export to .OLB File

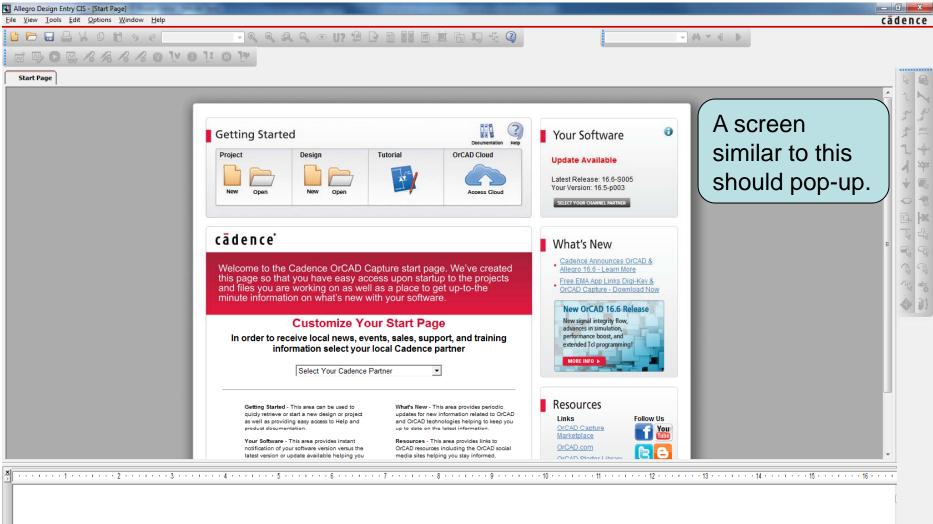




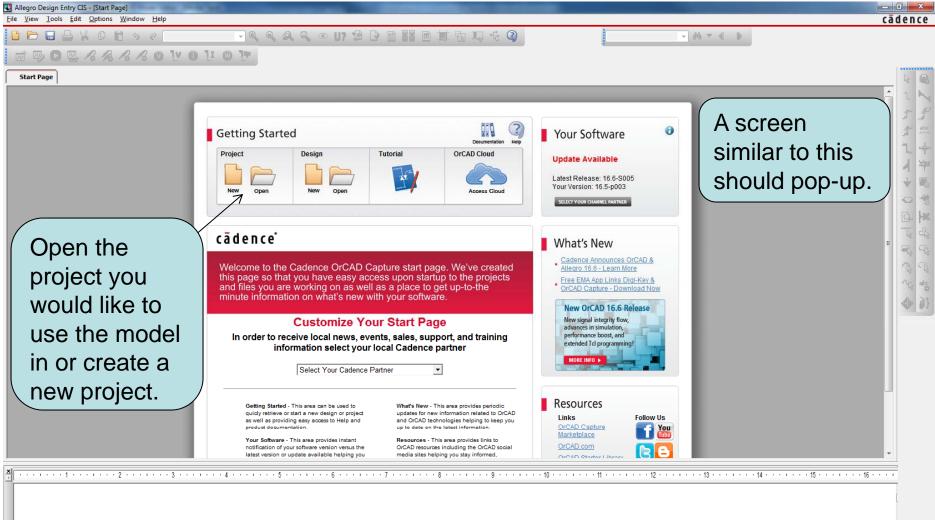
Run OrCAD. You can do this any way you are use to. If this is your first time go to Start \rightarrow Run and type in "capture", left click "OK."



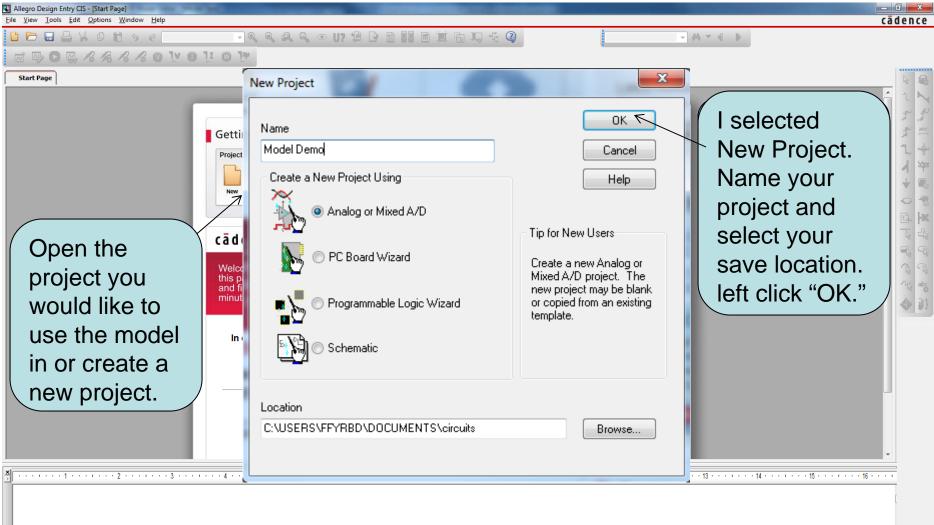








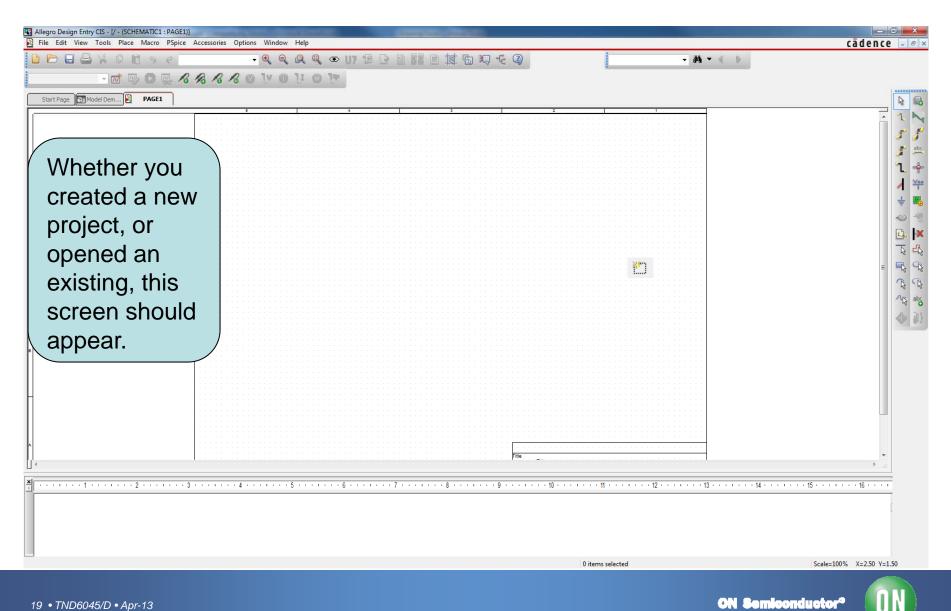






Done





19 • TND6045/D • Apr-13

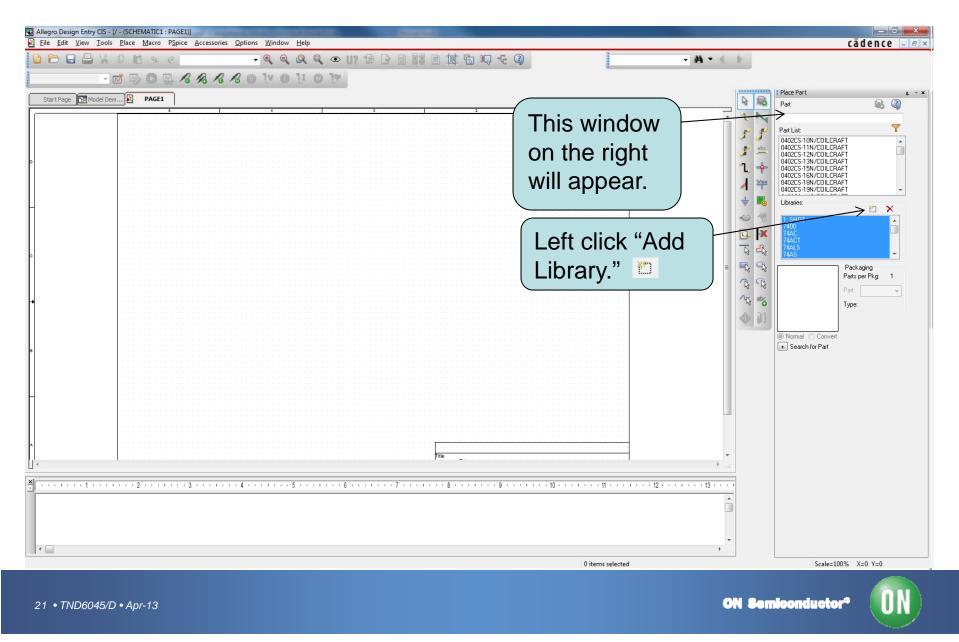
ON Semiconductor^o

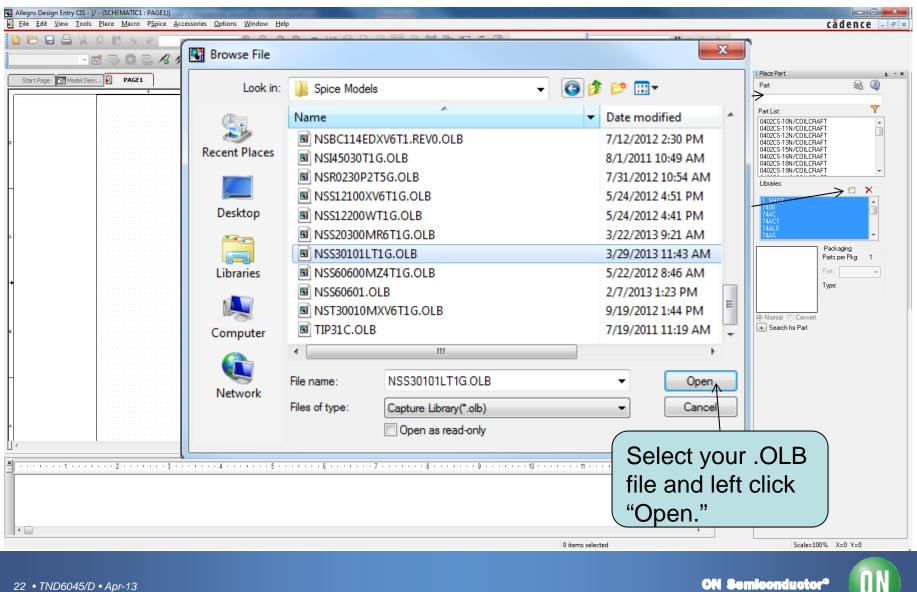
Allegro Design Entry CB - (/ - (SCHEMATIC1 + PAGE1)) File Edit View Tools Proc Macro PSoice Accessories Options Window Help		cădence - Ex
Allegro Design Entry CS - V - (SCHEMATIC1 : PAGE1)) ■ Ele: Edit View Iools Macro PSpice Accessories Options Window Help Parameterized Part. NetGroup U Qatabase Part Z Start Propr Model Des Auto Wire Bus 8 Auto Wire Bus 8 Auto Ion J Bus 8 Auton J Auton J Bus 8 Auton J Auton J A		
Bectangle Elippe Arc Elippical Arc Begier Curve Polyfine V Picture CleObject		· · ·
Place a part.(Shortcut Key: P)	0 items selected	Scale=100% X=-1.10 Y=0.10

20 • TND6045/D • Apr-13

ON Semiconductor^a







Allegro Design Entry CIS - [C:\USERS\FYRBD\DOCUMENTS\circuits\Model Demo.op] File Design Edit View Tools PSpice Accessories Reports Options Window Help		
C C C S < - < < < < < < < < < < < < < < < < < < <		
	_	
Start Page 🔝 Model De 😥 PAGE1	k 🚯	Place Part + ×
Analog or Mixed A/D		Part 🔒 🦓
C File 1% Hierarchy		Part List:
Design Resources Design Resources Design Resources Dubrary Dubrary Dubrars Discrete Resources Discrete Resources	デ 些 し 今 イ **	QNSS30101LT1G
Simulus Files		Libraies:
× · · · · · · · · · · · · · · · · · · ·		
Ready		

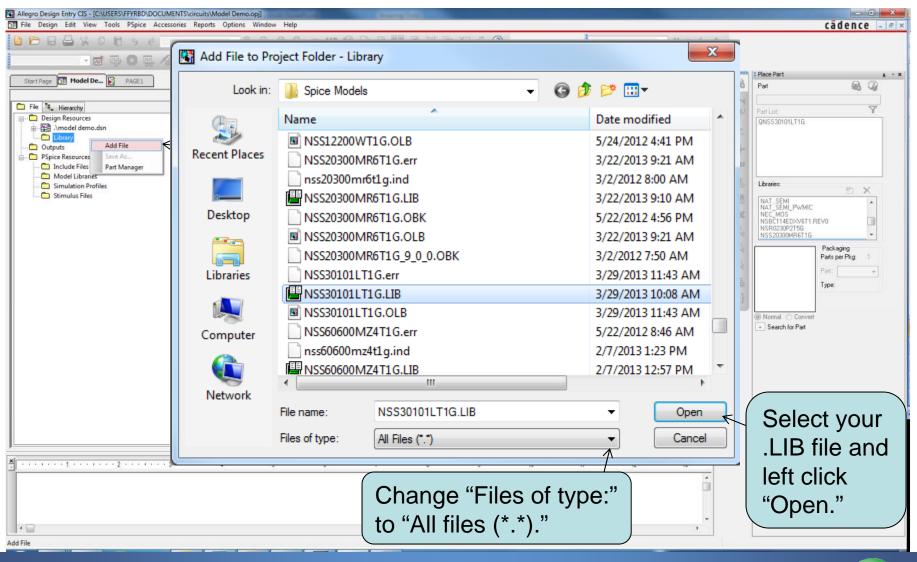


C Allegro Design Entry CIS - [CAUSERS\FYRBD\DOCUMENTS\circuits\Model Demo.op] T File Design Edit View Tools PSpice Accessories Reports Options Window Help		cādence – 🖛 🛛
Start Page 🔄 Model De 🔯 PAGE1		Place Part + * × Part 2
Analog or Mixed A/D	1.	
Image: Strategy interview Image: Strategy interview		Part List
Ready		

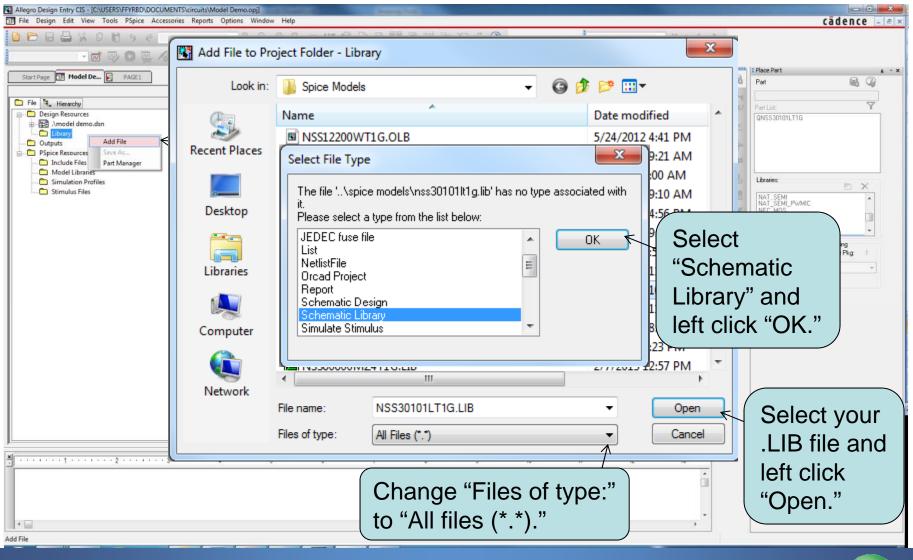


Allegro Design Entry CIS - [C:\USERS\FFYRBD\DOCUMENTS\circuits\Model Demo.op]		
TI File Design Edit View Tools PSpice Accessories Reports Options Window Help		cādence - 🖶 🛪
	P.	
Start Page T Model De PAGE1		1 Place Part a - ×
Start Page 35 Prode De. Pase 1 Analog or Mixed A/D	5 63	Part 🔬 🥥
File Ne, Hierarchy	1 7	Partiet Y
B Design Resources	55	Part List:
Barrier Amodel demo.dsn	# #L	
Cutputs Add File Left click "Add File."	1+	
T Include Files Part Manager	1 4	
Model Libraries Simulation Profiles	+ 15	Libraries:
C Stimulus Files	04	
	EL Her	NAT_SEMI NAT_SEMI_PWMIC NEC_MOS NSBC114EDXV6T1.REV0
	1	NSH8230P2156
	a a	NSS20300MR6T1G *
	AR	Packaging Parts per Pkg:
	18 - 8	Part 🖉 👻
	AL AL	Туре:
	令 街	
		Normal Convert
		+ Search for Part
	-	
≝,		
Add File	4.	

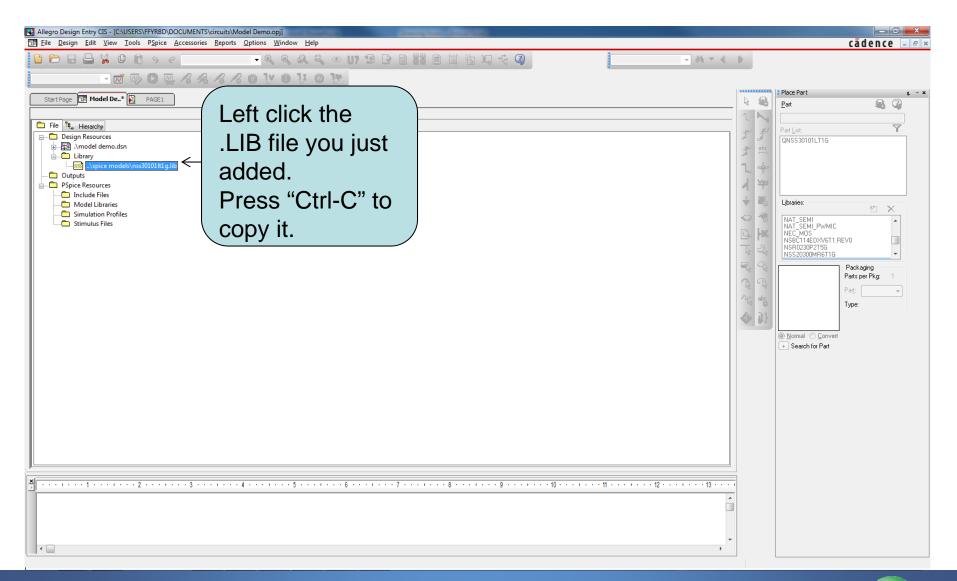




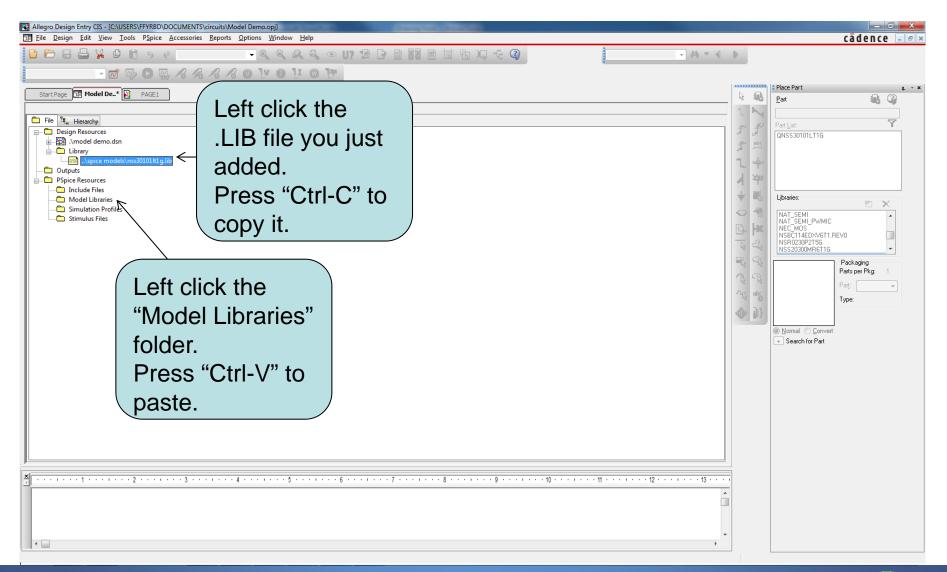








ON





Allegro Design Entry CIS - [C:\USERS\FFYRBD\DOCUMENTS\circuits\Model Demo.op]]		
In File Design Edit View Tools PSpice Accessories Reports Options Window Help		cādence – 📼 🗵
		: Place Part 📮 👻 🗙
Start Page T Model De* PAGE1	R 🚯	Part 🚯 🍘
Analog or Mixed A/D	1 📐	
Very Important! You will have to add the .LIB file for this model to every project you plan on using it in.		Part List: QNSS30101LT1G Libraries: NAT_SEMI NAT_SEMI_PWMIC NEC_MOS NSR020P2T5G NSR020P2T5G NSS20300MR6T1G Parts per Pkg: 1 Part: " Type: @ Normal © convert + Search for Part

ON