

POWER MANAGEMENT

High-Voltage EL Lamp Driver

- 220 V_{PP} Drive

The IMP528 is an Electroluminescent (EL) lamp driver with the four EL lamp driving functions on-chip. These are the switch-mode power supply, its high-frequency oscillator, the high-voltage H-bridge lamp driver and its low-frequency oscillator. The IMP528 drives EL lamps of up to 50nF capacitance to high brightness; EL lamps with capacitances greater than 50nF can be driven, but will be lower in light output. The typical regulated output voltage that is applied to the EL lamp is 220V peak-to-peak. The circuit requires few external components; a single inductor, single diode, two capacitors and three resistors. Two of these resistors set the frequency for two internal oscillators.

The IMP528 operates over a 2.0V to 6.5V supply voltage range. A regulated, low-power source can supply the low quiescent current of the IMP528. The inductor may be driven from an independent, unregulated supply voltage in dual supply applications.

An internal circuit shuts down the switching regulator when the lamp drive voltage reaches 220V peak-to-peak. This conserves power and extends battery life.

The IMP528 is available in MicroSO and SO-8 packages and in die or wafer form.

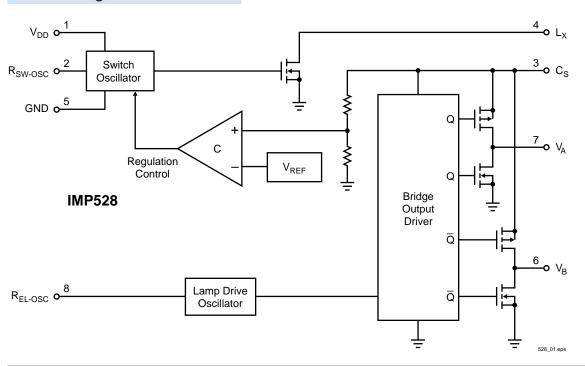
Key Features

- ◆ 220V peak-to-peak typical AC output voltage
- ♦ Low Power: 420µA typical V_{DD} current
- ♦ Wide operating voltage range-from 2.0V to 6.5V
- ◆ Large output load capability drives lamps with more than 50nF capacitance
- Adjustable output lamp frequency for control of lamp color, lamp life, and power consumption
- Adjustable converter frequency to minimize power consumption
- ◆ High-Voltage CMOS Process
- **♦** MicroSO package option

Applications

- ◆ GPS units/Pagers/Cellular phones
- ◆ PDAs/Handheld computers
- ◆ Safety illumination
- ◆ Portable instrumentation
- Battery-operated displays
- ◆ LCD modules
- ◆ Toys

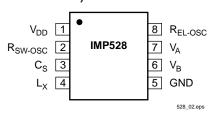
Block Diagram





Pin Configuration

SO/ MicroSO



Pin Compatible With IMP803 and IMP560

Ordering Information

Part Number	Input Voltage	Regulated Output Voltage	Temperature Range	Pins-Package
IMP528ESA	2.0V to 6.5V	Yes	-40°C to +85°C	8-SO
IMP528EMA	2.0V to 6.5V	Yes	-40°C to +85°C	8-MicroSO
IMP528/D	2.0V to 6.5V	Yes	25°C	Dice

Add /T to ordering part number for Tape and Reel.

Absolute Maximum Ratings

 Note: All voltages are referenced to GND.

These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability.

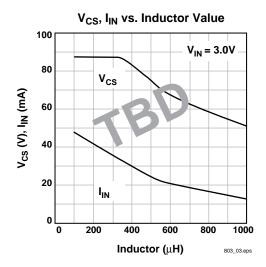
Electrical Characteristics

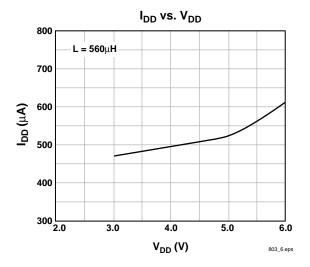
Unless otherwise noted, V_{DD} = 3.0V, R_{SW} = 750k Ω , R_{EL} = 2.0M Ω , and T_A = 25°C.

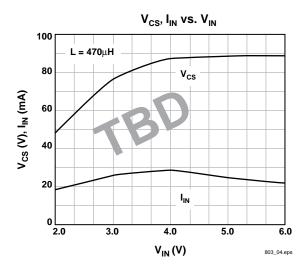
Parameter	Symbol	Conditions	Min	Тур	Max	Units
ON-resistance of MOS Switch	R _{DS(ON)}	I = 100mA		3.5	8	Ω
Output Voltage Regulation	V _{CS}	$V_{DD} = 2.0 \text{ to } 6.0 \text{V}$	95	110	115	V
Output Voltage Peak-to-peak (in regulation)	V _A -V _B	$V_{DD} = 2.0 \text{ to } 6.0 \text{V}$	190	220	230	V
Input Current at V _{DD} Pin	I _{DD}	V _{DD} = 3.0V, See Figure 1		420	700	μA
Input Current at V _{DD} Pin	I _{DD}	V _{DD} = 5.0V, See Figure 2		500	750	μA
Quiescent V _{DD} Supply Current, Disabled	I _{DDQ}	V _{RSW-OSC} <100mV		20	200	nA
Input Current: IDD Plus Inductor Current	I _{IN}	V _{DD} = 3.0V, See Figure 1		20	31	mA
Output Voltage at V _{CS}	V _{CS}	V _{DD} = 3.0V, See Figure 1	95	110	115	V
V _{A-B} Output Drive Frequency	f _{EL}	V _{DD} = 3.0V, See Figure 1	300	370	430	Hz
Switching Frequency	f _{SW}	V _{DD} = 3.0V, See Figure 1	50	70	90	kHz
Switching Duty Cycle	D _{SW}	V _{DD} = 3.0V, See Figure 1		88		%

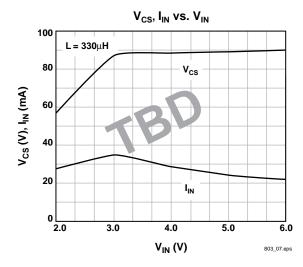


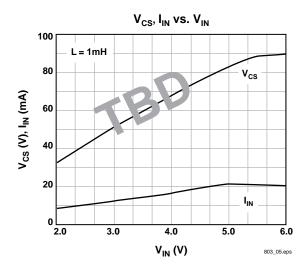
Typical Characteristics

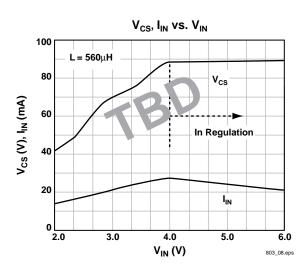












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Pin Descriptions

Pin Number	Name	Function
1	V_{DD}	Positive voltage supply for the IMP528. Inductor L may be connected here or to a separate unregulated supply.
2	R _{SW-OSC}	Switch-mode resistor pin. Switching frequency is determined by an external resistor, R _{SW} .
3	Cs	Boost converter storage capacitor. The voltage across the EL lamp is equal to twice the voltage at $C_{\rm S}$.
4	L _X	Connection to flyback inductance, L.
5	GND	Ground pin.
6	V_{B}	EL lamp drive. The lamp is connected in a high-voltage bridge circuit with V_B providing the complementary connection to V_A . The peak-to-peak AC voltage across the EL lamp is thus two times V_{CS} .
7	V _A	EL lamp drive. (See above)
8	R _{EL-OSC}	The EL lamp oscillator frequency setting pin. The oscillator frequency is controlled by external resistor R _{EL} .

Detailed Descriptions

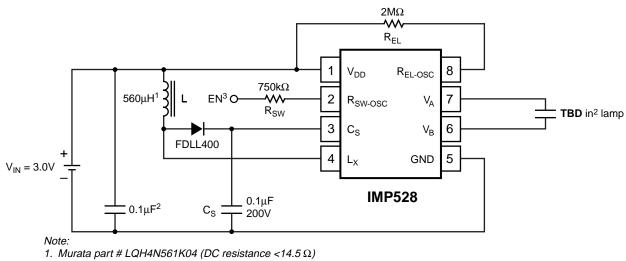
External Component	Description and Selection Guide
Diode	Catch diode. A fast reverse recovery diode, with BV > 120V, such as an FDLL400 (150V).
Capacitor C _S	This is the high voltage capacitor that stores the inductive energy transferred through the catch diode. A capacitor with WV > 120V between 10nF and 100nF is recommended.
Resistor R _{EL}	The EL lamp oscillator frequency setting resistor. This resistor, connected between the $R_{EL\text{-}OSC}$ pin and V_{DD} , provides an oscillator frequency inversely proportional to R_{EL} ; as R_{EL} increases, the EL lamp frequency decreases along with the current drawn by the lamp. Lamp color is also determined by this frequency. A $2M\Omega$ resistor between the $R_{EL\text{-}OSC}$ pin and the V_{DD} supply results in a lamp frequency around 350Hz: a $1M\Omega$ resistor will give \approx 700Hz.
Resistor R _{SW}	Switching Oscillator frequency setting resistor. The switching oscillator resistor is connected between the R_{SW-OSC} pin and the V_{DD} supply. The switching frequency is inversely proportional to the resistor value, dropping as the resistance increases.
Inductor L	The inductor provides the voltage boost needed by means of inductive "flyback". The internal MOSFET switch alternately opens and closes the ground connection for the inductor at the L_X pin. When this internal switch opens, the inductor potential will forward-bias the catch diode and the current will pass through the storage capacitor C_S , charging it to a high voltage.
	Smaller inductors are preferred to prevent saturation. As the value of the inductor increases (and the series DC resistance of the inductor decreases), the switching frequency set by R _{SW} should be increased to prevent saturation. In general, smaller value inductors that can handle more current are more desirable when larger area EL lamps must be driven.



Application Information

Test and Application Circuit, 3.0V

Figure 1 shows the IMP528 configured to drive a **TBD**-square-inchEL lamp, represented as a **TBD**nF capacitor. With a 3.0V input, the EL lamp will be driven to moderate brightness.



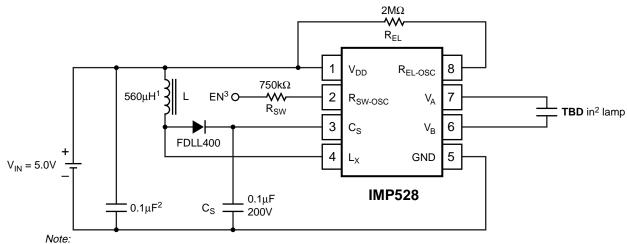
- 2. Larger values may be required depending upon supply impedance.
- 3. EN is connected to V_{DD} to enable and to GND to disable.

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Figure 1. 3.0V Application

Test and Application Circuit, 5.0V

 $\it Figure~2$ shows a 5.0V input application driving a TBD-square-inch EL lamp.



- 1. Murata part # LQH4N561K04 (DC resistance <14.5 Ω)
- 2. Larger values may be required depending upon supply impedance.
- 3. EN is connected to V_{DD} to enable and to GND to disable.

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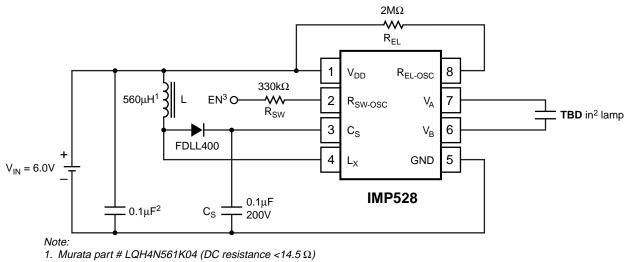
Figure 2. 5.0V Application

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Test and Application Circut, 6.0V

At higher input voltage levels, the IMP528 will drive large EL lamps. *Figure 3* shows a 6.0V circuit configuration that will drive a **TBD** square-inch lamp.



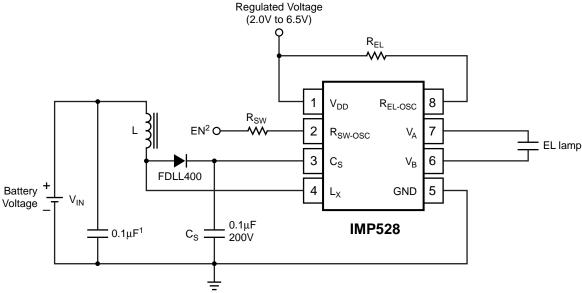
- 2. Larger values may be required depending upon supply impedance.
- 3. EN is connected to V_{DD} to enable and to GND to disable.

Figure 3. 6.0V Application

Dual Supply Operation with 1.5V Battery

The IMP528 can also be operate from a single battery cell when a regulated voltage higher than 2.0V is also available. This dual supply configuration, shown in *Figure 4*, uses the regulated voltage to operate the IMP528 while the energy for the high-voltage boost circuit comes from the battery.

The circuit of *Figure 4* thus allows operation with batteries that are below the 2V minimum specification or above the 6.5V maximum operating voltage.



- 1. Larger values may be required depending upon supply impedance.
- 2. EN is connected to V_{DD} to enable and to GND to disable.

Figure 4. Dual Supply Operation

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High-Voltages Present

Switch Resistance

The IMP528 inductor switch resistance is typically below 3.5Ω , as shown in *Figure 5*.

The IMP528 generates high voltages and caution should be exercised.

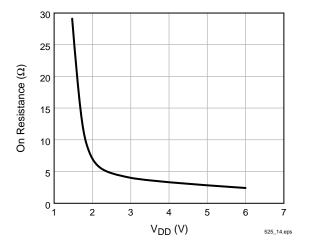
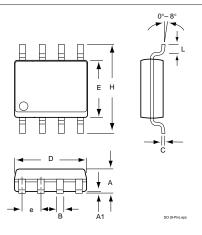


Figure 5. Boost Switch ON-Resistance

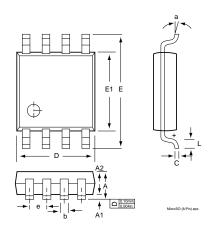


Package Dimensions

SO (8-Pin)



MicroSO (8-Pin)



Inches			Millimeters			
	Min	Max	Min	Max		
	SO (8-Pin)*					
Α	0.053	0.069	1.35	1.75		
A1	0.004	0.010	0.10	0.25		
В	0.013	0.020	0.33	0.51		
С	0.007	0.010	0.19	0.25		
е	0.0)50	1.27			
Е	0.150	0.157	3.80	4.00		
Н	0.228	0.244	5.80	6.20		
L	0.016	0.050	0.40	1.27		
D	0.189	0.197	4.80	2.00		
	MicroSO (8-Pin)**					
Α		0.0433		1.10		
A1	0.0020	0.0059	0.050	0.15		
A2	0.0295	0.0374	0.75	0.95		
b	0.0098	0.0157	0.25	0.40		
С	0.0051	0.0091	0.13	0.23		
D	0.1142	0.1220	2.90	3.10		
е	0.0256 BSC		0.65 BSC			
Е	0.193 BSC		4.90 BSC			
E1	0.1142	0.1220	2.90	3.10		
L	0.0157	0.0276	0.40	0.70		
а	0°	6°	0°	6°		

* JEDEC Drawing MS-012AA

** JEDEC Drawing MO-187AA



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