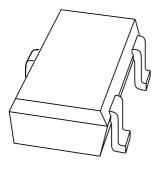
DISCRETE SEMICONDUCTORS

DATA SHEET



1PS70SB20Schottky barrier diode

Product data sheet 2001 Mar 16



Schottky barrier diode

1PS70SB20

FEATURES

- · Ultra high switching speed
- Low forward voltage
- · Guard ring protected
- Small SMD plastic package.

APPLICATIONS

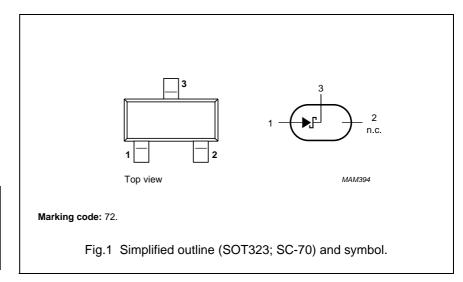
- Ultra high-speed switching
- Voltage clamping
- Protection circuits.

PINNING

PIN	DESCRIPTION
1	anode
2	not connected
3	cathode

DESCRIPTION

Planar Schottky barrier diode with an integrated guard ring for stress protection in a SOT323 (SC-70) small SMD plastic package.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _R	continuous reverse voltage	_	_	40	V
I _F	continuous forward current	-	_	500	mA
I _{FSM}	non-repetitive peak forward current	t = 8.3 ms half sine wave; JEDEC method	_	2	А
T _{stg}	storage temperature	_	-65	+150	°C
Tj	junction temperature	_	_	125	°C

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ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _F	forward voltage	I _F = 500 mA; see Fig.2	_	550	mV
I_R	reverse current	V _R = 35 V; see Fig.3	_	100	μΑ
		$V_R = 35 \text{ V}; T_j = 100 ^{\circ}\text{C}; \text{ see Fig.3};$ note 1	_	10	mA
C _d	diode capacitance	$f = 1 \text{ MHz}$; $V_R = 0$; see Fig.4	60	90	pF

Note

1. Pulse test: t_p = 300 μ s; δ = 0.02.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

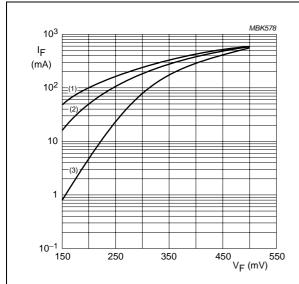
Note

1. Refer to SOT323 (SC-70) standard mounting conditions.

Schottky barrier diode

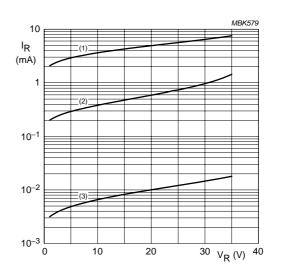
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GRAPHICAL DATA



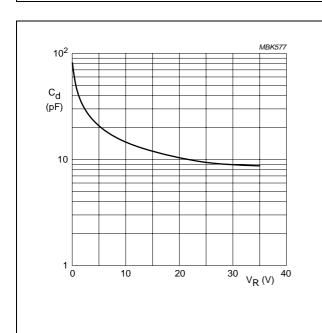
- (1) $T_{amb} = 125 \, ^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.2 Forward current as a function of forward voltage; typical values.



- (1) $T_{amb} = 125 \, ^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.3 Reverse current as a function of reverse voltage; typical values.



f = 1 MHz; T_{amb} = 25 °C.

Fig.4 Diode capacitance as a function of reverse voltage; typical values.

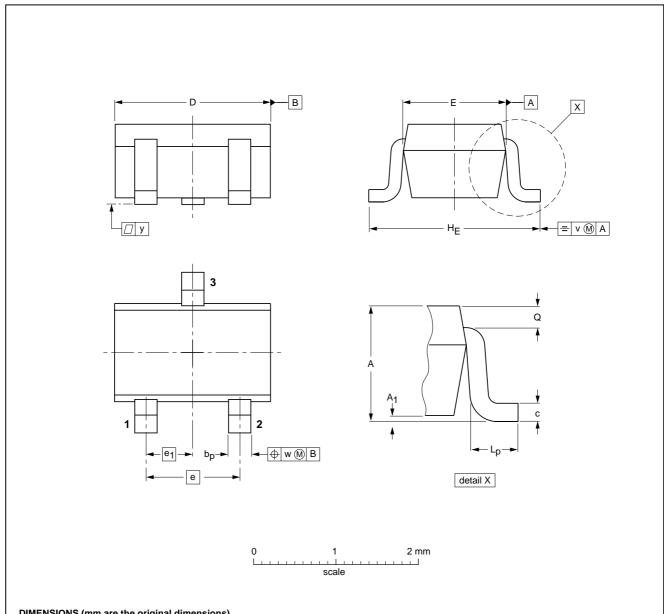
Schottky barrier diode

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



DIMENS	10N2 (II	ım are tı	ie origii	iai dime	nsions)	
						ī

UNIT	Α	A ₁ max	bp	С	D	E	е	e ₁	HE	Lp	Q	v	w
mm	1.1 0.8	0.1	0.4 0.3	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE		REFER	EUROPEAN ISSUE DATE			
VERSION	IEC	JEDEC	JEDEC EIAJ PROJECTION		1330E DATE	
SOT323			SC-70		$ \ \ \bigoplus \big($	97-02-28

Schottky barrier diode

1PS70SB20

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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