## BCX17LT1, BCX18LT1, PNP BCX19LT1, NPN

## General Purpose Transistors

Voltage and Current are Negative for PNP Transistors

## Features

- Pb-Free Packages are Available


## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Collector - Emitter Voltage | $\mathrm{V}_{\mathrm{CEO}}$ |  | Vdc |
| BCX17LT1, BCX19LT1 |  | 45 |  |
| BCX18LT1 |  | 25 |  |
| Collector - Base Voltage | $\mathrm{V}_{\mathrm{CBO}}$ |  | Vdc |
| BCX17LT1, BCX19LT1 |  | 50 |  |
| BCX18LT1 |  | 30 |  |
| Emitter - Base Voltage | $\mathrm{V}_{\text {EBO }}$ | 5.0 | Vdc |
| Collector Current - Continuous | $\mathrm{I}_{\mathrm{C}}$ | 500 | mAdc |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
| :---: | :---: | :---: | :---: |
| Total Device Dissipation FR-5 Board <br> (Note 1), $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ <br> Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | 225 | mW |
| Thermal Resistance, <br> Junction-to-Ambient | $\mathrm{R}_{\text {ӨJA }}$ | 556 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Total Device Dissipation Alumina <br> Substrate, (Note 2 $2 \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ <br> Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | 300 | mW |
| Thermal Resistance, <br> Junction-to-Ambient | $\mathrm{R}_{\theta \mathrm{OA}}$ | 417 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Junction and Storage Temperature | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $F R-5=1.0 \times 0.75 \times 0.062 \mathrm{in}$.
2. Alumina $=0.4 \times 0.3 \times 0.024$ in $99.5 \%$ alumina.

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ORDERING INFORMATION
See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

ELECTRICAL CHARACTERISTICS $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic |  | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS |  |  |  |  |  |  |
| Collector-Emitter Breakdown Voltage $\left(I_{C}=10 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=0\right)$ | $\begin{aligned} & \text { BCX17, } 19 \\ & \text { BCX18 } \end{aligned}$ | $\mathrm{V}_{\text {(BR)CEO }}$ | $\begin{aligned} & 45 \\ & 25 \end{aligned}$ | - | - | Vdc |
| Collector-Emitter Breakdown Voltage $\left(\mathrm{I}_{\mathrm{C}}=10 \mu \mathrm{Adc}, \mathrm{I}_{\mathrm{C}}=0\right)$ | $\begin{aligned} & \text { BCX17, } 19 \\ & \text { BCX18 } \end{aligned}$ | $\mathrm{V}_{\text {(BR)CES }}$ | $\begin{aligned} & 50 \\ & 30 \end{aligned}$ | - | - | Vdc |
| $\begin{aligned} & \text { Collector Cutoff Current } \\ & \qquad\left(V_{C B}=20 \mathrm{Vdc}, \mathrm{I}_{\mathrm{E}}=0\right) \\ & \left(\mathrm{V}_{\mathrm{CB}}=20 \mathrm{Vdc}, \mathrm{I}_{\mathrm{E}}=0, \mathrm{~T}_{\mathrm{A}}=150^{\circ} \mathrm{C}\right) \end{aligned}$ |  | $\mathrm{I}_{\text {CBO }}$ | - | - | $\begin{aligned} & 100 \\ & 5.0 \end{aligned}$ | nAdc uAdc |
| Emitter Cutoff Current $\left(\mathrm{V}_{\mathrm{EB}}=5.0 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=0\right)$ |  | Iebo | - | - | 10 | $\mu \mathrm{Adc}$ |

ON CHARACTERISTICS

| $\begin{aligned} & \text { DC Current Gain } \\ & \left(\mathrm{I}_{\mathrm{C}}=100 \mathrm{mAdc}, \mathrm{~V}_{\mathrm{CE}}=1.0 \mathrm{Vdc}\right) \\ & \left(\mathrm{I}_{\mathrm{C}}=300 \mathrm{mAdc}, \mathrm{~V}_{\mathrm{CE}}=1.0 \mathrm{Vdc}\right) \\ & \left(\mathrm{I}_{\mathrm{C}}=500 \mathrm{mAdc}, \mathrm{~V}_{\mathrm{CE}}=1.0 \mathrm{Vdc}\right) \end{aligned}$ | $\mathrm{h}_{\text {FE }}$ | $\begin{aligned} & 100 \\ & 70 \\ & 40 \end{aligned}$ |  | $600$ | - |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Collector-Emitter Saturation Voltage ( $\mathrm{I}_{\mathrm{C}}=500 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=50 \mathrm{mAdc}$ ) | $\mathrm{V}_{\mathrm{CE} \text { (sat) }}$ | - | - | 0.62 | Vdc |
| $\begin{aligned} & \text { Base-Emitter On Voltage } \\ & \quad\left(\mathrm{I}_{\mathrm{C}}=500 \mathrm{mAdc}, \mathrm{~V}_{C E}=1.0 \mathrm{Vdc}\right) \end{aligned}$ | $V_{\text {BE(on) }}$ | - | - | 1.2 | Vdc |

ORDERING INFORMATION

| Device | Specific Marking | Package | Shipping ${ }^{\text { }}$ |
| :---: | :---: | :---: | :---: |
| BCX17LT1 | T1 | SOT-23 | 3000 / Tape \& Reel |
| BCX17LT1G |  | $\begin{gathered} \hline \text { SOT-23 } \\ \text { (Pb-Free) } \end{gathered}$ | 3000 / Tape \& Reel |
| BCX17LT3 |  | SOT-23 | 10,000 / Tape \& Reel |
| BCX17LT3G |  | $\begin{gathered} \hline \text { SOT-23 } \\ \text { (Pb-Free) } \end{gathered}$ | 10,000 / Tape \& Reel |
| BCX18LT1 | T2 | SOT-23 | 3000 / Tape \& Reel |
| BCX18LT1G |  | $\begin{gathered} \hline \text { SOT-23 } \\ \text { (Pb-Free) } \end{gathered}$ | 3000 / Tape \& Reel |
| BCX19LT1 | U1 | SOT-23 | 3000 / Tape \& Reel |
| BCX19LT1G |  | $\begin{aligned} & \hline \text { SOT-23 } \\ & \text { (Pb-Free) } \end{aligned}$ | 3000 / Tape \& Reel |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## BCX17LT1, BCX18LT1, PNP BCX19LT1, NPN



Figure 1. DC Current Gain


Figure 2. Saturation Region


Figure 4. Temperature Coefficients


Figure 3. "On" Voltages


Figure 5. Capacitances

## PACKAGE DIMENSIONS

SOT-23 (TO-236)
CASE 318-08
ISSUE AN


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THICKNESSIS TH
4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08

|  | MILLIMETERS |  |  | INCHES |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 0.89 | 1.00 | 1.11 | 0.035 | 0.040 | 0.044 |
| A1 | 0.01 | 0.06 | 0.10 | 0.001 | 0.002 | 0.004 |
| b | 0.37 | 0.44 | 0.50 | 0.015 | 0.018 | 0.020 |
| c | 0.09 | 0.13 | 0.18 | 0.003 | 0.005 | 0.007 |
| D | 2.80 | 2.90 | 3.04 | 0.110 | 0.114 | 0.120 |
| E | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 |
| e | 1.78 | 1.90 | 2.04 | 0.070 | 0.075 | 0.081 |
| L | 0.10 | 0.20 | 0.30 | 0.004 | 0.008 | 0.012 |
| L1 | 0.35 | 0.54 | 0.69 | 0.014 | 0.021 | 0.029 |
| HE | 2.10 | 2.40 | 2.64 | 0.083 | 0.094 | 0.104 |

STYLE 6:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

## SOLDERING FOOTPRINT*


*For additional information on our $\mathrm{Pb}-$ Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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## PUBLICATION ORDERING INFORMATION

## LITERATURE FULFILLMENT

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