Low Noise Transistors

PNP Silicon

Features

• Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-45	Vdc
Collector-Base Voltage	V _{CEB}	-50	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	۱ _C	-100	mAdc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	P _D	625 5.0	mW mW/°C
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

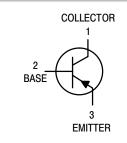
Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



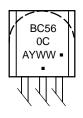
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



A = Assembly Location

= Year

Y

WW = Work Week

= Pb–Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping
BC560C	TO-92	5000 Units / Bulk
BC560CG	TO-92 (Pb-Free)	5000 Units / Bulk
BC560CZL1	TO-92	2000 / Ammo Pack
BC560CZL1G	TO–92 (Pb–Free)	2000 / Ammo Pack

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

BC560C

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage $(I_{C} = -10 \text{ mAdc}, I_{B} = 0)$	V _{(BR)CEO}	-45	_	_	Vdc
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	-50	_	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = -10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	12	-	-	Vdc
Collector Cutoff Current ($V_{CB} = -30 \text{ Vdc}, I_E = 0$) ($V_{CB} = -30 \text{ Vdc}, I_E = 0, T_A = +125^{\circ}C$)	I _{CBO}			-15 -5.0	nAdc μAdc
Emitter Cutoff Current ($V_{EB} = -4.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	_	-15	nAdc
ON CHARACTERISTICS					
DC Current Gain $(I_{C} = -10 \ \mu Adc, \ V_{CE} = -5.0 \ Vdc)$ $(I_{C} = -2.0 \ mAdc, \ V_{CE} = -5.0 \ Vdc)$	h _{FE}	100 380	270 500	_ 800	-
	V _{CE(sat)}		-0.075 -0.3 -0.25	-0.25 -0.6 -	Vdc
Base – Emitter Saturation Voltage ($I_c = -100 \text{ mAdc}, I_B = -5.0 \text{ mAdc}$)	V _{BE(sat)}	_	-1.1	-	Vdc
$\begin{array}{l} \text{Base-Emitter On Voltage} \\ (I_{C} = -10 \ \mu \text{Adc}, \ V_{CE} = -5.0 \ \text{Vdc}) \\ (I_{C} = -100 \ \mu \text{Adc}, \ V_{CE} = -5.0 \ \text{Vdc}) \\ (I_{C} = -2.0 \ \text{mAdc}, \ V_{CE} = -5.0 \ \text{Vdc}) \end{array}$	V _{BE(on)}	_ _ _0.55	-0.52 -0.55 -0.62	_ 	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current–Gain – Bandwidth Product ($I_C = -10$ mAdc, $V_{CE} = -5.0$ Vdc, f = 100 MHz)	f _T	_	250	-	MHz
Collector–Base Capacitance ($V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C _{cbo}	-	2.5	-	pF
Small–Signal Current Gain ($I_C = -2.0 \text{ mAdc}$, $V_{CE} = -5.0 \text{ V}$, f = 1.0 kHz)	h _{fe}	450	600	900	pF
Noise Figure $(I_C = -200 \ \mu \text{Adc}, \ V_{CE} = -5.0 \ \text{Vdc}, \ R_S = 2.0 \ \text{k}\Omega, \ f = 1.0 \ \text{kHz})$ $(I_C = -200 \ \mu \text{Adc}, \ V_{CE} = -5.0 \ \text{Vdc}, \ R_S = 100 \ \text{k}\Omega, \ f = 1.0 \ \text{kHz}, \ \Delta f = 200 \ \text{kHz})$	$NF_1 NF_2$	_ _	0.5 -	2.0 10	dB

1. I_B is value for which I_C = -11 mA at V_{CE} = -1.0 V. 2. Pulse test = 300 μ s - Duty cycle = 2%.

BC560C

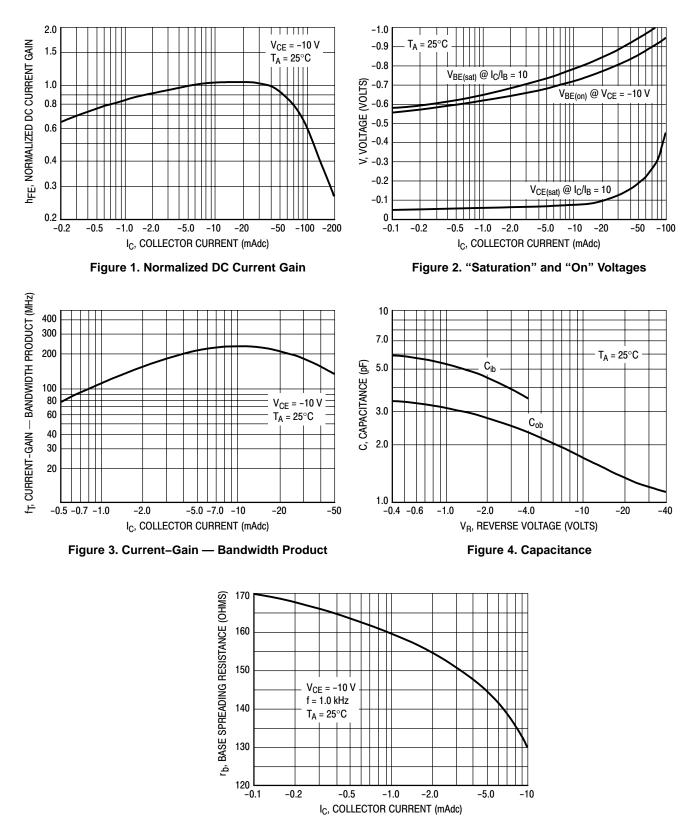
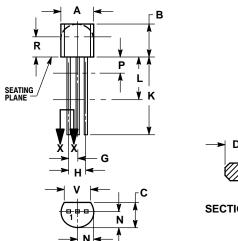


Figure 5. Base Spreading Resistance

BC560C

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

2.

CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 3.

LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 17:

PIN 1. COLLECTOR 2. BASE

3. EMITTER

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.

BC560C/D