MMSD103T1

Preferred Device

High Voltage Switching Diode

Features

• Pb–Free Package is Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V _R	250	Vdc
Peak Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	625	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Forward Power Dissipation, FR–5 Board (Note 1) @ $T_A = 25^{\circ}C$ Derate above 25°C	P _F 400 3.2		mW mW/°C
Thermal Resistance, Junction-to-Case	$R_{\theta JL}$	174	°C/W
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	492	°C/W
Junction Temperature	Τ _J	125 Max	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

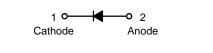
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.



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SOD-123 CASE 425 STYLE 1

MARKING DIAGRAM



JS = Device Code M = Date Code = Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMSD103T1	SOD-123	3,000 / Tape & Reel
MMSD103T1G	SOD-123 (Pb-Free)	3,000 / Tape & Reel

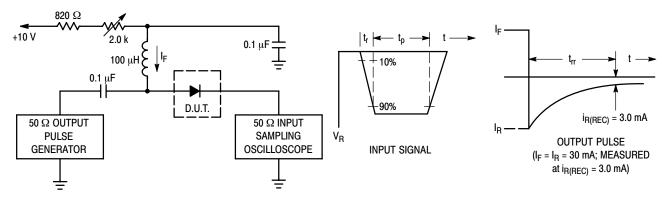
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

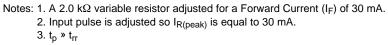
Preferred devices are recommended choices for future use and best overall value.

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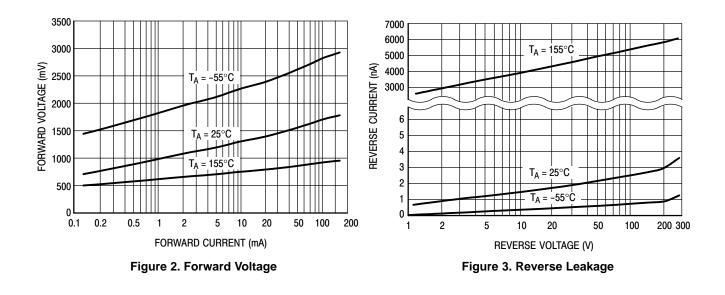
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Voltage Leakage Current $(V_R = 200 \text{ Vdc})$ $(V_R = 200 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I _R		1.0 100	μAdc
Reverse Breakdown Voltage (I _{BR} = 100 μAdc)	$V_{(BR)}$	250	-	Vdc
Forward Voltage $(I_F = 100 \text{ mAdc})$ $(I_F = 200 \text{ mAdc})$	V _F		1000 1250	mV
Diode Capacitance ($V_R = 0, f = 1.0 \text{ MHz}$)	CD	-	5.0	pF
Reverse Recovery Time $(I_F = I_R = 30 \text{ mAdc}, R_L = 100 \Omega)$	t _{rr}	_	50	ns





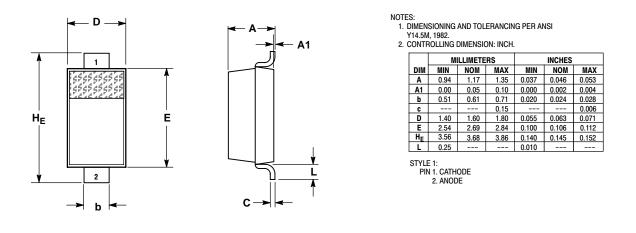




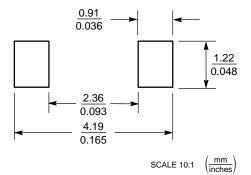
MMSD103T1

PACKAGE DIMENSIONS

SOD-123 CASE 425-04 ISSUE E



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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