Switching Diode

The switching diode has the following features:

Features

- SOD-123 Surface Mount Package
- High Breakdown Voltage
- Fast Speed Switching Time
- Pb–Free Package is Available



Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V _R	80	Vdc
Peak Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note1) $T_A = 25^{\circ}C$ Derate above 25°C	P _D	425 3.4	mW mW/°C
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	290	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. FR-5 = 1.0oz Cu, 1.0in^z pad



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SOD-123 CASE 425 STYLE 1

MARKING DIAGRAM



6S = Device Code M = Date Code • = Pb-Free Package (Note: Microdot may be in either location)

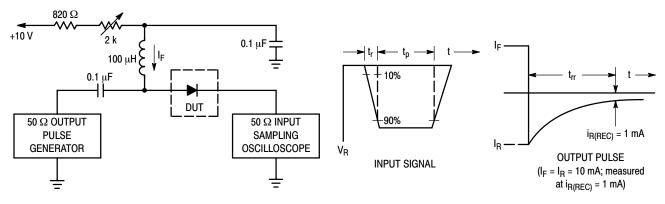
ORDERING INFORMATION

Device	Package	Shipping [†]	
MMSD71RKT1	SOD-123	3000 / Tape & Reel	
MMSD71RKT1G	SOD-123 (Pb-Free)	3000 / Tape & Reel	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage $(I_{BR} = 100 \mu\text{Adc})$	V _(BR)	80	-	Vdc
Reverse Voltage Leakage Current ($V_R = 80 \text{ Vdc}$)	Ι _R	-	500	nAdc
Forward Voltage (I _F = 100 mAdc)	V _F	-	1200	mVdc
Diode Capacitance ($V_R = 0.5 \text{ Vdc}, f = 1.0 \text{ MHz}$)	CD	-	2.0	pF
Reverse Recovery Time ($I_F = I_R = 10 \text{ mAdc}$) (Figure 1)	t _{rr}	-	4.0	ns



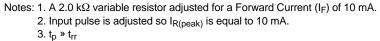


Figure 1. Recovery Time Equivalent Test Circuit

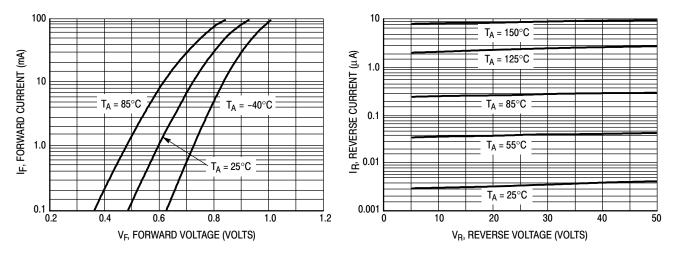




Figure 3. Leakage Current

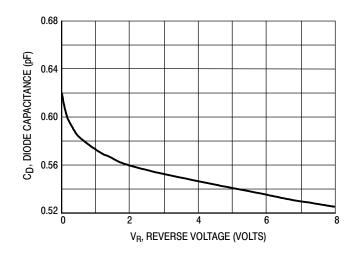
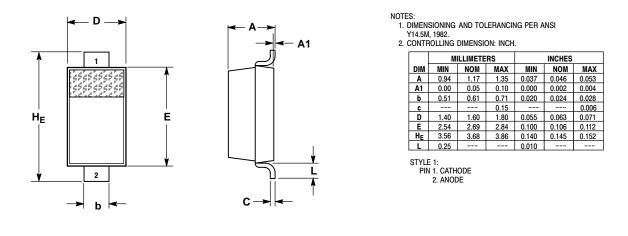


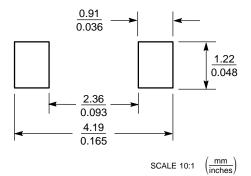
Figure 4. Capacitance

PACKAGE DIMENSIONS

SOD-123 CASE 425-04 ISSUE E



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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