PNP General Purpose Transistor

The MMBT2907AM3T5G device is a spin-off of our popular SOT-23 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- Reduces Board Space
- This is a Halide–Free Device
- This is a Pb–Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-60	Vdc
Collector – Base Voltage	V _{CBO}	-60	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ι _C	-600	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^{\circ}C$ Derate above 25°C	P _D	265 2.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	470	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	640 5.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	195	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

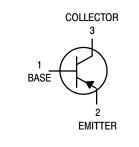
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

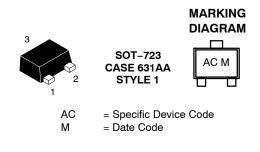
2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.



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ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT2907AM3T5G	SOT-723 (Pb-Free)	•

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	<u>.</u>			
Collector – Emitter Breakdown Voltage (Note 3) $(I_{\rm C} = -10 \text{ mAdc}, I_{\rm B} = 0)$	V _{(BR)CEO}	-60	-	Vdc
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	-60	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = -10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)	ICEX	_	-50	nAdc
Collector Cutoff Current ($V_{CB} = -50$ Vdc, $I_E = 0$) ($V_{CB} = -50$ Vdc, $I_E = 0$, $T_A = 125^{\circ}C$)	I _{CBO}		-0.010 -10	μAdc
Base Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)	I _{BL}	-	-50	nAdc
ON CHARACTERISTICS				
DC Current Gain (I _C = -0.1 mAdc, V _{CE} = -10 Vdc)	h _{FE}	75	-	-

		75 100 100 100 50	- - 300 -	
Collector – Emitter Saturation Voltage (Note 3) ($I_C = -150 \text{ mAdc}$, $I_B = -15 \text{ mAdc}$) (Note 3) ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$)	V _{CE(sat)}		-0.4 -1.6	Vdc
Base – Emitter Saturation Voltage (Note 3) ($I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}$)	V _{BE(sat)}		-1.3 -2.6	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current-Gain – Bandwidth Product (Notes 3, 4) (I _C = –50 mAdc, V _{CE} = –20 Vdc, f = 100 MHz)	f _T	200	_	MHz
Output Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	8.0	pF
Input Capacitance (V _{EB} = -2.0 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	30	

SWITCHING CHARACTERISTICS

Turn–On Time		t _{on}	-	45	
Delay Time	(V _{CC} = −30 Vdc, I _C = −150 mAdc, I _{B1} = −15 mAdc)	t _d	-	10	
Rise Time	. ,	t _r	-	40	
Turn–Off Time		t _{off}	-	100	ns
Storage Time	(V _{CC} = -6.0 Vdc, I _C = -150 mAdc, I _{B1} = I _{B2} = -15 mAdc)	ts	-	80	
Fall Time	, ,	t _f	-	30	

 $\begin{array}{ll} \mbox{3. Pulse Test: Pulse Width} \leq 300 \ \mu \mbox{s, Duty Cycle} \leq 2.0\%. \\ \mbox{4. } f_T \mbox{ is defined as the frequency at which } |h_{fe}| \ extrapolates to unity. \\ \end{array}$

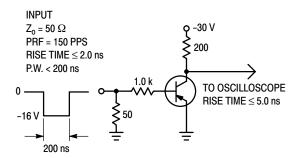
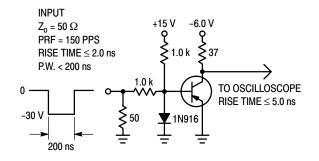


Figure 1. Delay and Rise Time Test Circuit





TYPICAL CHARACTERISTICS

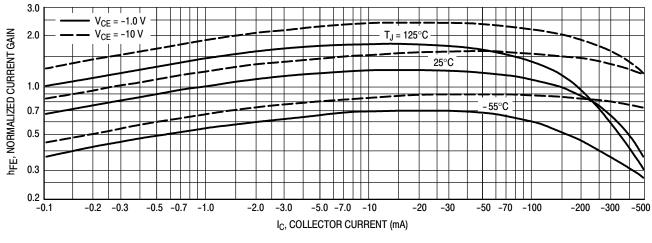
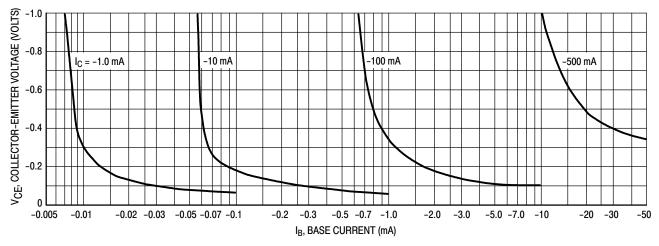
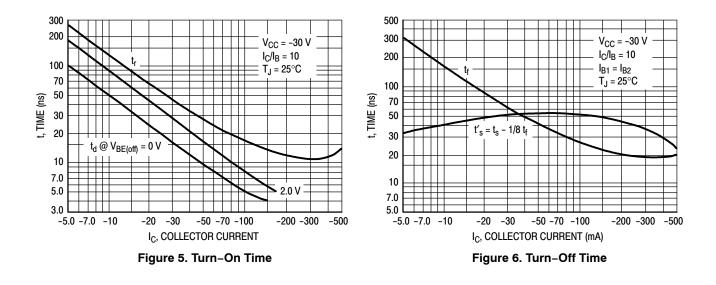


Figure 3. DC Current Gain

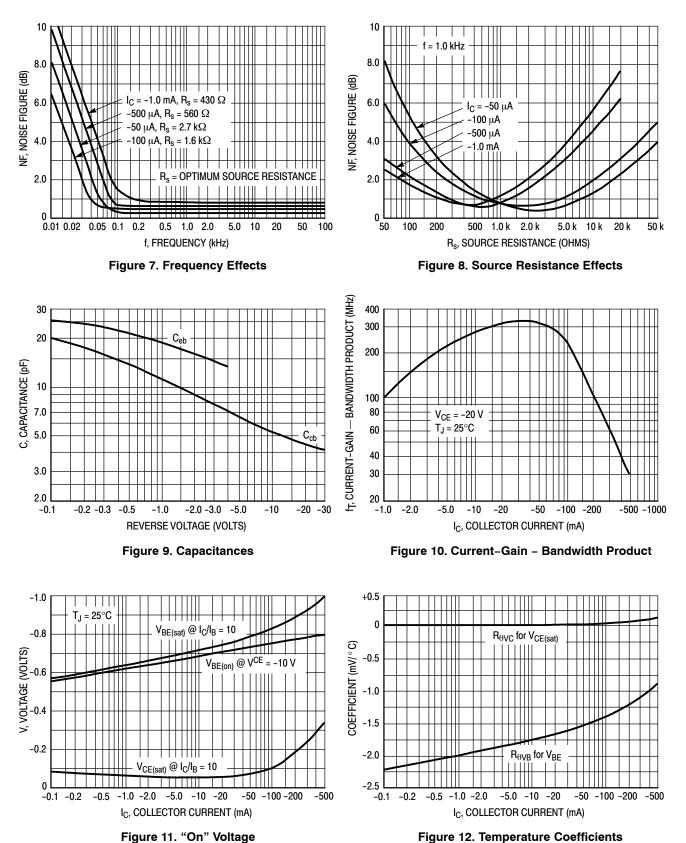






TYPICAL SMALL-SIGNAL Characteristics NOISE FIGURE

 V_{CE} = 10 Vdc, T_A = 25°C



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PACKAGE DIMENSIONS

SOT-723 CASE 631AA-01 **ISSUE C**

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.
- CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD 2
- З. THICKNESS OF BASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD
- 4. FLASH, PROTRUSIONS OR GATE BURRS.

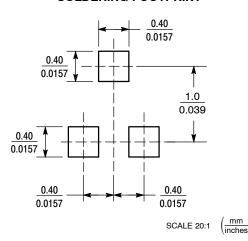
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
b	0.15	0.21	0.27	0.0059	0.0083	0.0106
b1	0.25	0.31	0.37	0.010	0.012	0.015
С	0.07	0.12	0.17	0.0028	0.0047	0.0067
D	1.15	1.20	1.25	0.045	0.047	0.049
E	0.75	0.80	0.85	0.03	0.032	0.034
е		0.40 BS	SC	0.016 BSC		
ΗE	1.15	1.20	1.25	0.045	0.047	0.049
L	0.15	0.20	0.25	0.0059	0.0079	0.0098

-Yb 2X С \oplus 0.08 (0.0032) Х Υ

-X-

SOLDERING FOOTPRINT*

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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