NUP2114UPXV5

Low Capacitance Diode-TVS Array for High Speed Data Line Protection

The NUP2114UPXV5 transient voltage suppressor is designed to protect high speed data lines from ESD. Ultra-low capacitance and high level of ESD protection makes this device well suited for use in USB 2.0 applications.

Features

- Low Capacitance (0.8 pF Typical Between I/O Lines)
- ESD Rating of Class 3B (Exceeding 8 kV) per Human Body model and Class C (Exceeding 400 V) per Machine Model
- Protection for the Following IEC Standards: IEC 61000-4-2 13 kV (contact)
- UL Flammability Rating of 94 V-0
- This is a Pb-Free Device

Typical Applications

- High Speed Communication Line Protection
- USB 2.0 High Speed Data Line and Power Line Protection
- Monitors and Flat Panel Displays
- MP3
- Gigabit Ethernet
- Notebook Computers
- Digital Video Interface (DVI) and HDMI

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

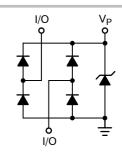
Rating	Symbol	Value	Unit
Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Seconds)	TL	260	°C
Human Body Model (HBM) Machine Model (MM) IEC 61000-4-2 Contact (ESD)	ESD	16000 400 13000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

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SOT-553 CASE 463B





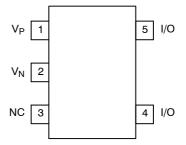
P2 = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



ORDERING INFORMATION

Device	Package	Shipping
NUP2114UPXV5T1G	SOT-553 (Pb-Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NUP2114UPXV5

ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise specified)

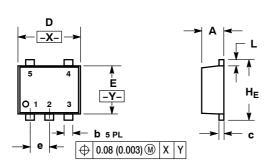
Parameter	r Symbol Conditions		Min	Тур	Max	Unit
Reverse Working Voltage	V_{RWM}	(Note 1)			5.0	V
Breakdown Voltage	V_{BR}	I _T = 1 mA, (Note 2)	6.0			V
Reverse Leakage Current	I _R	V _{RWM} = 5 V			1.0	μΑ
Junction Capacitance	CJ	V _R = 0 V, f = 1 MHz between I/O Pins and GND		0.8	1.0	pF
Junction Capacitance	CJ	V _R = 0 V, f = 1 MHz between I/O Pins		0.4	0.5	pF

TVS devices are normally selected according to the working peak reverse voltage (V_{RWM}), which should be equal or greater than the DC or continuous peak operating voltage level.
 V_{BR} is measured at pulse test current I_T.

NUP2114UPXV5

PACKAGE DIMENSIONS

SOT-553, 6 LEAD CASE 463B-01 **ISSUE B**

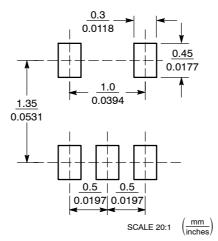


NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES
 LEAD FINISH THICKNESS MINIMUM LEAD
 THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL

	MILLIMETERS					
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.50	0.55	0.60	0.020	0.022	0.024
b	0.17	0.22	0.27	0.007	0.009	0.011
С	0.08	0.13	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.063	0.067
E	1.10	1.20	1.30	0.043	0.047	0.051
е		0.50 BSC			0.020 BSC	
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	1.50	1.60	1.70	0.059	0.063	0.067

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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