

## SANYO Semiconductors

### DATA SHEET

N-Channel Silicon MOSFET

# SCH1434 — General-Purpose Switching Device Applications

#### **Features**

- 1.8V drive
- · Halogen free compliance

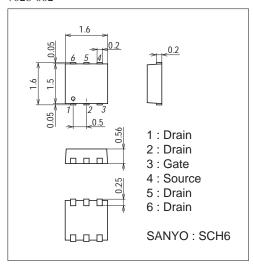
#### **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		30	V
Gate-to-Source Voltage	VGSS		±12	V
Drain Current (DC)	ID		2	А
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	8	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm <sup>2</sup> x0.8mm)	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Package Dimensions**

unit : mm (typ) 7028-002



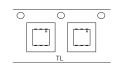
#### **Product & Package Information**

• Package : SCH6

• JEITA, JEDEC :-

• Minimum Packing Quantity : 5,000 pcs./reel

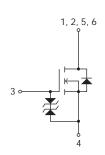
#### Packing Type: TL



# ZK NO.

Marking

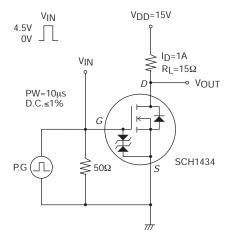
#### **Electrical Connection**

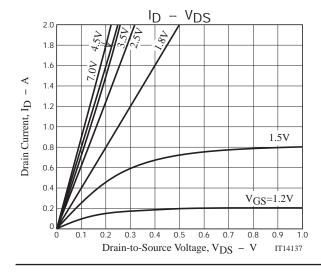


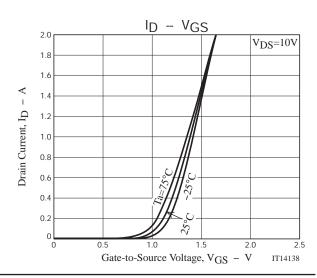
#### Electrical Characteristics at Ta=25°C

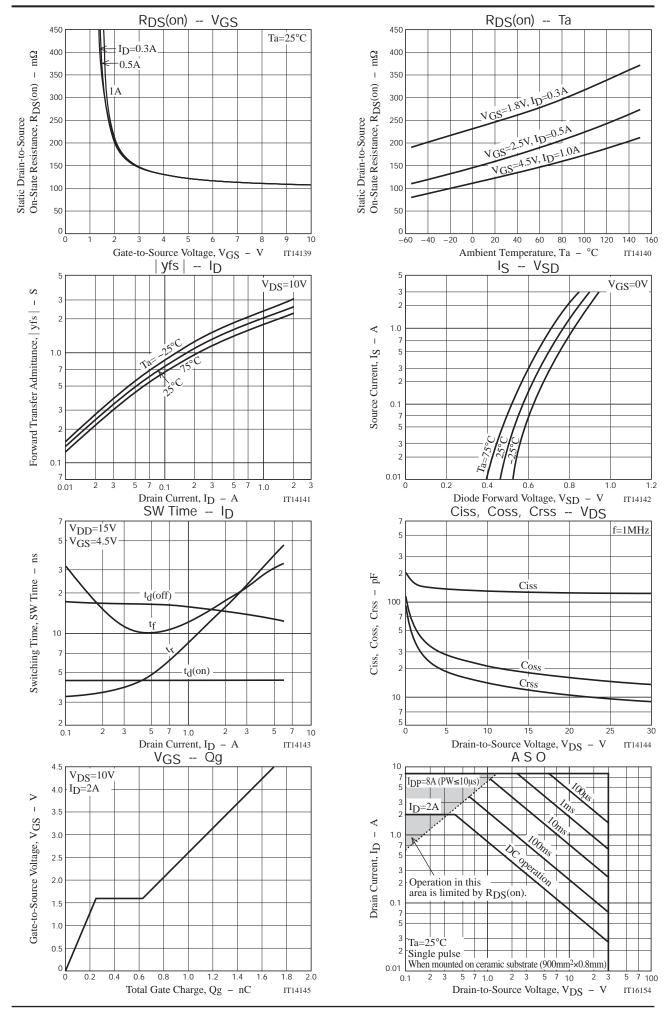
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V <sub>D</sub> S=10V, I <sub>D</sub> =1A		2.0		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =1A, V <sub>G</sub> S=4.5V		125	165	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =0.5A, V <sub>G</sub> S=2.5V		165	235	mΩ
	R <sub>DS</sub> (on)3	I <sub>D</sub> =0.3A, V <sub>G</sub> S=1.8V		250	375	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		130		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		21		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		14		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		4.4		ns
Rise Time	tr	See specified Test Circuit.		8.7		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		16		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit.		12		ns
Total Gate Charge	Qg	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A		1.7		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A		0.25		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A		0.38		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2A, V <sub>GS</sub> =0V		0.85	1.2	V

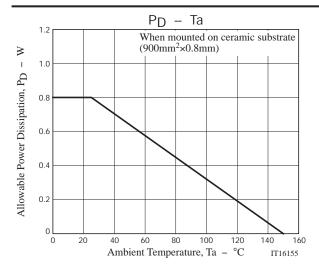
#### **Switching Time Test Circuit**











Note on usage: Since the SCH1434 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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