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FAIRCHILD

SEMICONDUCTOR

November 2013

FQL40N50 N-Channel QFET® MOSFET 500 V, 40 A, 110 mΩ

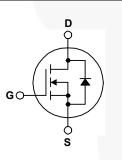
Description

This N-Channel enhancement mode power MOSFET is • 40 A, 500 V, $R_{DS(on)}$ = 110 m Ω (Max.) @ V_{GS} = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state $I_D = 20 \text{ A}$ Low Gate Charge (Typ. 155 nC) resistance, and to provide superior switching performance . Low Crss (Typ. 95 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

Features

- $I_{D} = 20 \text{ A}$





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

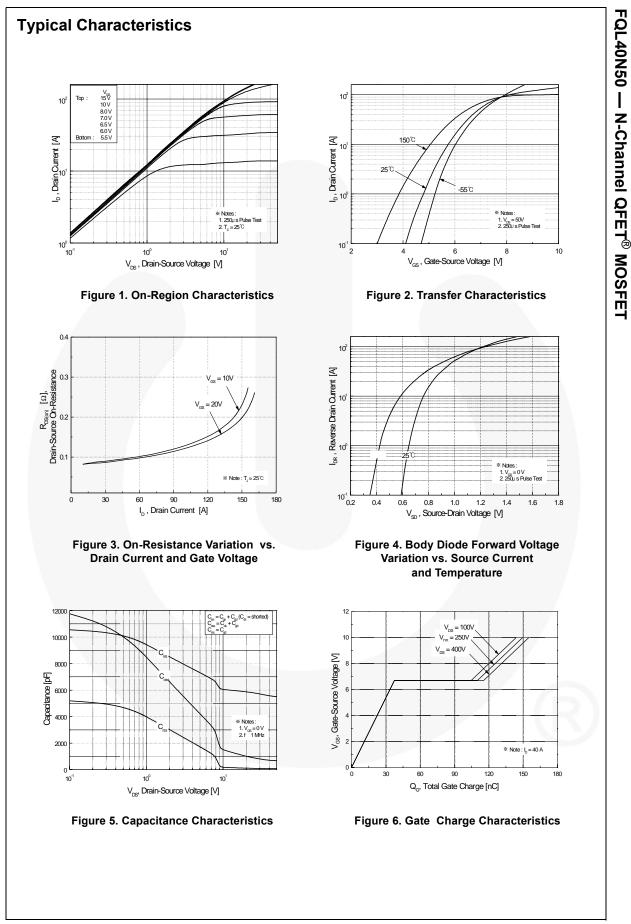
Symbol	Parameter		FQL40N50	Unit V	
V _{DSS}	Drain-Source Voltage		500		
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		40	A	
	- Continuous (T _C = 100°C)		25	A	
I _{DM}	Drain Current - Pulsed	(Note 1)	160	A	
V _{GSS}	Gate-Source Voltage		± 30	V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	1780	mJ	
I _{AR}	Avalanche Current	(Note 1)	40	A	
E _{AR}	Repetitive Avalanche Energy	(Note 1)	46	mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns	
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		460	W	
	- Derate above 25°C		3.7	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum lead temperature for soldering, 1/8" from case for 5 seconds.		300	°C	

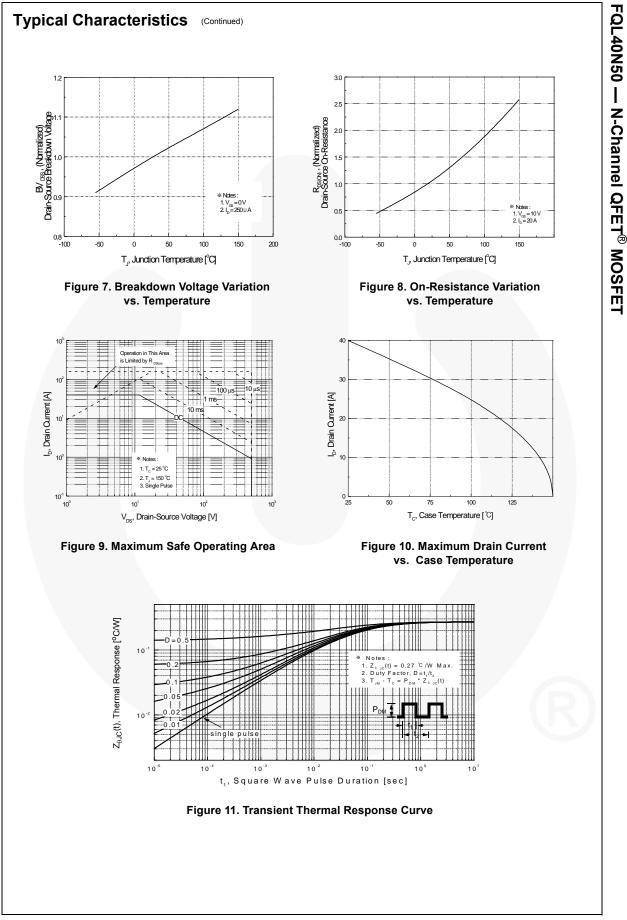
Thermal Characteristics

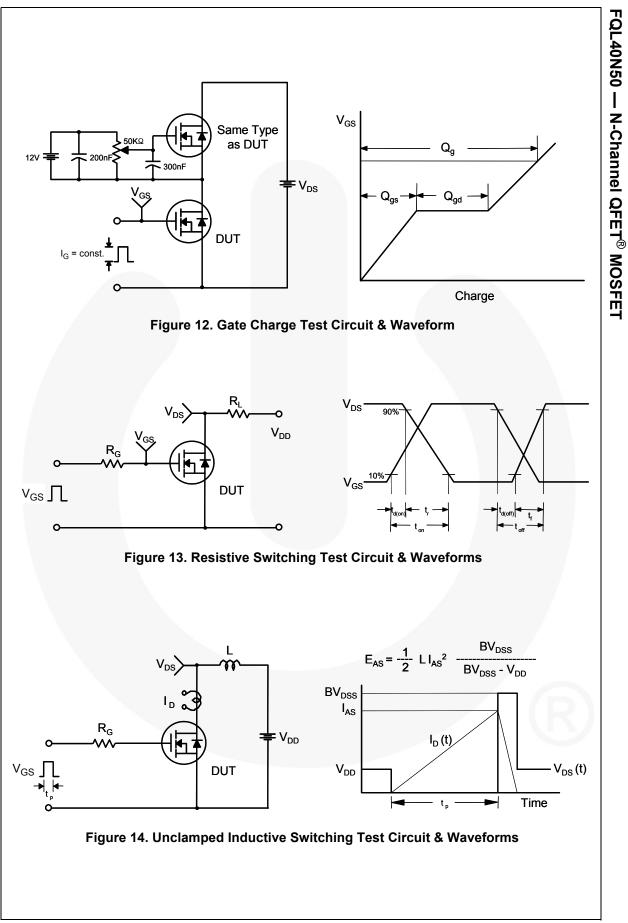
Symbol	Parameter	FQL40N50	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.27	°C/W	
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	30	°C/W	

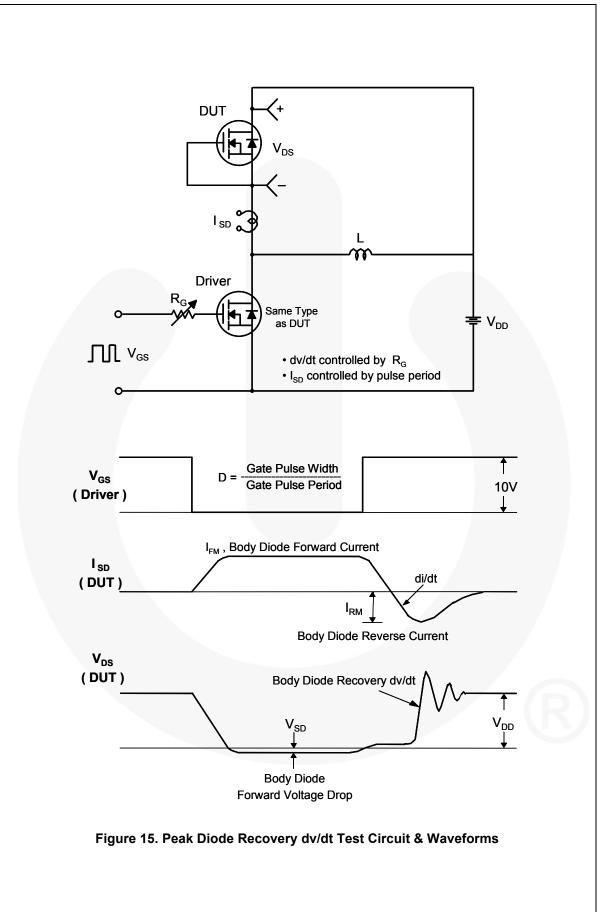
Part Number FQL40N50		Top Mark	Top Mark Package		Packing Method Reel		Size	Tape Width		Quantity	
		FQL40N50 T		-264 Tube N/			A	N/A		25 units	
lectric	al Cha	racteristics	T _C = 25°C	Cunless ot	herwise noted.						
Symbol		Parameter	0		Test Cond	litions		Min.	Тур.	Max.	Unit
Off Cha	ractoriet	tice									
BV _{DSS}	aracteristics Drain-Source Breakdown Voltage			V _{GS} =	0 V, I _D = 250	Ο μΑ		500			V
ΔΒV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient		$I_D = 250 \ \mu\text{A}$, Referenced to 25°C				0.48		V/°C		
I _{DSS}	Zero Gate Voltage Drain Current		$V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 400 \text{ V}, T_C = 125^{\circ}\text{C}$					1	μΑ		
								10	μΑ		
I _{GSSF}	Gate-Body Leakage Current, Forward		$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$					100	nA		
I _{GSSR}	Gate-Bod	y Leakage Current,	Reverse	V _{GS} =	-30 V, V _{DS} =	= 0 V				-100	nA
On Cha	racterist	ics									
V _{GS(th)}		eshold Voltage		V _{DS} =	$V_{GS}, I_{D} = 25$	60 μA		3.0		5.0	V
R _{DS(on)}	Static Dra On-Resis	in-Source tance		V _{GS} = 10 V, I _D = 20 A			0.085	0.11	Ω		
9fs	Forward 7	Forward Transconductance $V_{DS} = 50 \text{ V}, I_D = 20 \text{ A}$				A			29		S
Dunomi	ia Chara	atoriation									
C _{iss}	Input Cap	cteristics	_		05.14.14	<u> </u>			5800	7500	pF
C _{oss}		apacitance		_ V _{DS} = 25 V, V _{GS} = f = 1.0 MHz		0 V,			880	1150	pF
C _{rss}	-	Fransfer Capacitance	۵.	1 = 1.0					95	120	pF
	I		<u> </u>							120	P
		acteristics		1				1			
t _{d(on)}		Delay Time	_	V _{DD} = 250 V, I _D		40 A,			140	290	ns
t _r	Turn-On F		_	$R_{G} = 2$	$R_{G} = 25 \Omega$			440	890	ns	
t _{d(off)}		Delay Time		-		(N)			350	700	ns
t _f	Turn-Off F						lote 4)		250	500	ns
Q _g	Total Gate	ů.		-	400 V, I _D = 4	40 A,			155	200	nC
Q _{gs}		rce Charge		V _{GS} =	10 V	()	lote 4)		37 78		nC
Q _{gd}	Gate-Drai	in Charge				(1	1018 4)		10		nC
Drain-S	ource Di	iode Characteri	stics a	nd Max	ximum Ra	tinas					
I _S	Fource Diode Characteristics and Maximum Ratings Maximum Continuous Drain-Source Diode Forward Current							40	Α		
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current							160	Α		
V _{SD}	Drain-Sou	Irce Diode Forward	Voltage	$V_{GS} = 0 V, I_{S} = 40 A$					1.4	V	
t _{rr}	Reverse I	Recovery Time		$V_{GS} = 0 V, I_S = 40 A,$			520		ns		
Q _{rr}	Reverse I	Recovery Charge		dl _F / dt = 100 A/μs				8.0		μC	
. L = 2.0 mH, . I _{SD} \leq 40 A,	I_{AS} = 40 A, V _D di/dt \leq 200 A	dth limited by maximum jur $_{D}$ = 50 V, R_{G} = 25 Ω , startin (μs , $V_{DD} \le BV_{DSS}$, startin operating temperature.	ng T _J = $25^{\circ}C$).							

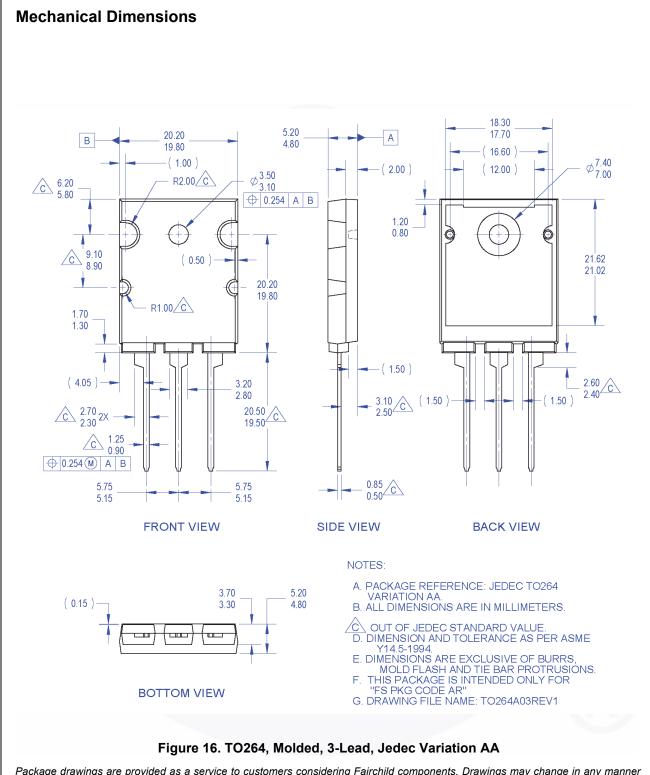
FQL40N50 — N-Channel QFET[®] MOSFET











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FQL40N50 — N-Channel QFET[®] MOSFET



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