MOSFET – Power, N-Channel, SUPERFET[®] III

800 V, 360 mΩ, 13 A

NTPF360N80S3Z

Description

800 V SUPERFET III MOSFET is ON Semiconductor's high performance MOSFET family offering 800 V breakdown voltage.

New 800 V SUPERFET III MOSFET which is optimized for primary switch of flyback converter, enables lower switching losses and case temperature without sacrificing EMI performance thanks to its optimized design. In addition, internal Zener Diode significantly improves ESD capability.

This new family of 800 V SUPERFET III MOSFET enables to make more efficient, compact, cooler and more robust applications because of its remarkable performance in switching power applications such as Laptop adapter, Audio, Lighting, ATX power and industrial power supplies.

Features

- Typ. $R_{DS(on)} = 300 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 25.3 nC)
- Low Stored Energy in Output Capacitance (Eoss = 2.72 μJ @ 400 V)
- 100% Avalanche Tested
- ESD Improved Capability with Zener Diode
- RoHS Compliant

Applications

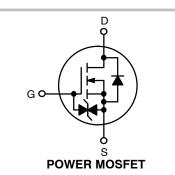
- Adapters / Chargers
- LED Lighting
- AUX Power
- Audio
- Industrial Power

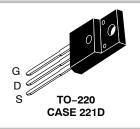


ON Semiconductor®

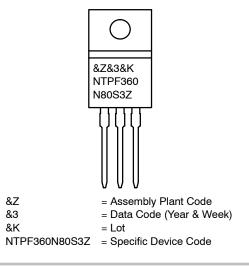
www.onsemi.com

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX	
800 V	$360 \text{ m}\Omega$	13 A	





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Par	Value	Unit V	
V _{DSS}	Drain-to-Source Voltage			
V _{GS}	Gate-to-Source Voltage	DC	±20	V
		AC (f > 1 Hz)	±30	
ID	Drain Current	Continuous (T _C = 25°C)	13*	А
		Continuous (T _C = 100°C)	8.2*	
I _{DM}	Drain Current Pulsed (Note 1)		32.5*	А
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		40	mJ
I _{AS}	Avalanche Current (Note 2)		2.0	А
E _{AR}	Repetitive Avalanche Energy (Note 1)		0.31	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		10	
PD	Power Dissipation	(T _C = 25°C)	31	W
		Derate Above 25°C	0.168	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +150	°C
ΤL	Lead Temperature Soldering Reflow for Soldering Purposes (1/8" from Case for 10 seconds)		260	°C

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C, unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
*Drain current limited by maximum junction temperature
1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. I_{AS} = 2.0 A, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 3.25 A, di/dt ≤ 200 A/µs, V_{DD} ≤ 400 V, starting T_J = 25°C.

THERMAL RESISTANCE RATINGS

Symbol	Parameter	Value	Unit
$R_{ ext{ heta}JC}$	Junction-to-Case - Steady State	4.04	°C/W
R_{\thetaJA}	R _{0JA} Junction-to-Ambient - Steady State		

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTPF360N80S3Z	NTPF360N80S3Z	TO-220F	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

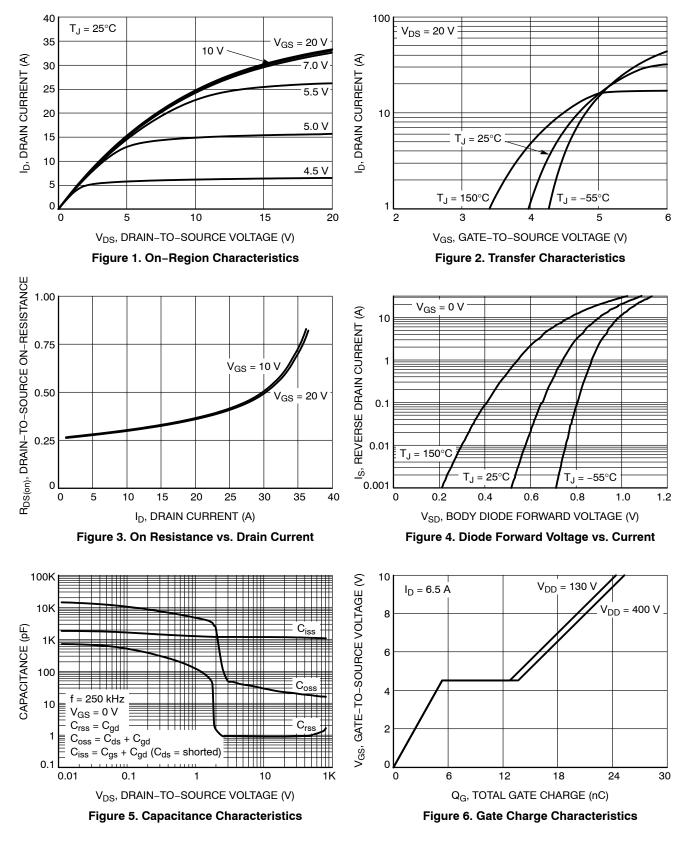
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS	•				
BV _{DSS}	Drain-to-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = 1 mA, T_J = 25^{\circ}C$ 800				V
		V_{GS} = 0 V, I_{D} = 1 mA, T_{J} = 150°C	900	1		V
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	$I_D = 1$ mA, Referenced to $25^{\circ}C$		1.1		V/°C
I _{DSS} Zero Gate	Zero Gate Voltage Drain Current	$V_{DS} = 800 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		1	1	μA
		V_{DS} = 640 V, T_{C} = 125°C		0.8		
I _{GSS}	Gate-to-Body Leakage Current	V_{GS} = ± 20 V, V_{DS} = 0 V		1	1	μA
ON CHARACTE	ERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.3 \text{ mA}$	2.2		3.8	V
R _{DS(on)}	Static Drain-to-Source On Resistance	V _{GS} = 10 V, I _D = 6.5 A		300	360	mΩ
9fs	Forward Transconductance	V _{DS} = 20 V, I _D = 6.5 A		13.8		S
DYNAMIC CHA	RACTERISTICS	•				
C _{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 250 kHz		1143		pF
C _{oss}	Output Capacitance			18.1		pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		236.4		pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		34		pF
Q _{g(tot)}	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$		25.3		nC
Q _{gs}	Gate-to-Source Gate Charge	(Note 4)		5.3		nC
Q _{gd}	Gate-to-Drain "Miller" Charge			8.3		nC
ESR	Equivalent Series Resistance	f = 1 MHz		4		Ω
SWITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V},$		21.2		ns
t _r	Turn-On Rise Time	R _g = 25 Ω (Note 4)		18.5		ns
t _{d(off)}	Turn-Off Delay Time			110		ns
t _f	Turn-Off Fall Time			17.7		ns
SOURCE-DRAI	N DIODE CHARACTERISTICS					
۱ _S	Maximum Continuous Source-to-Drain Diode Forward Current				13	Α
I _{SM}	Maximum Pulsed Source-to-Drain Diode Forward Current				32.5	Α
V _{SD}	Source-to-Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 6.5 A$			1.2	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 V, I_{SD} = 3.25 A,$		370		ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/µs		3.0		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

SUPERFET is a registered trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

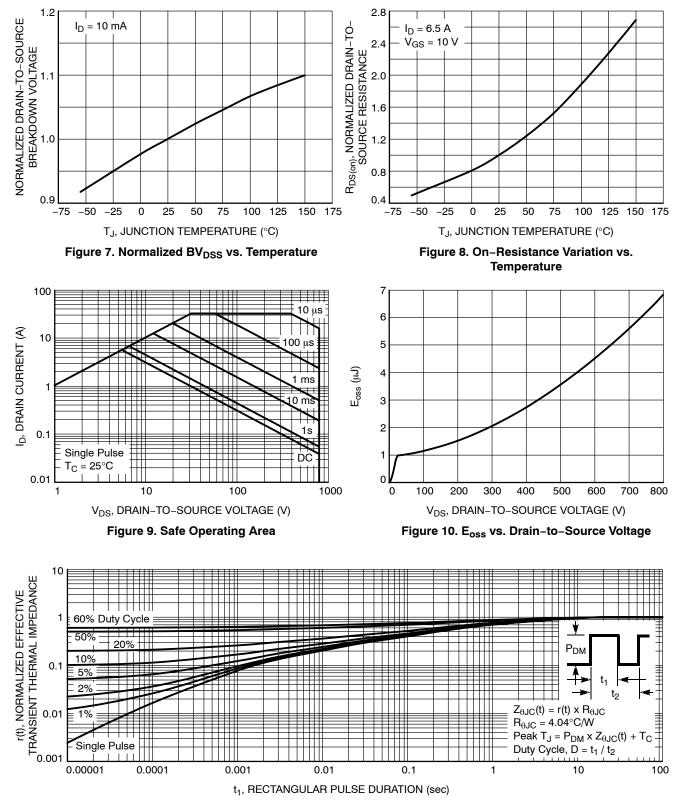
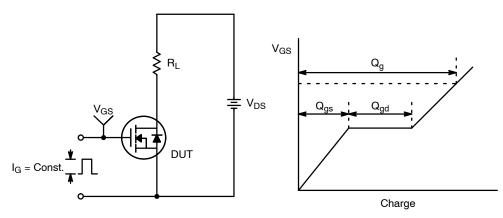


Figure 11. Transient Thermal Impedance





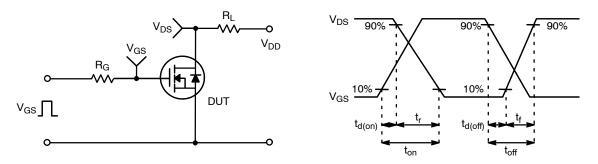


Figure 13. Resistive Switching Test Circuit & Waveforms

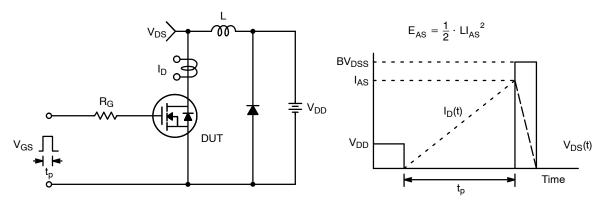


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

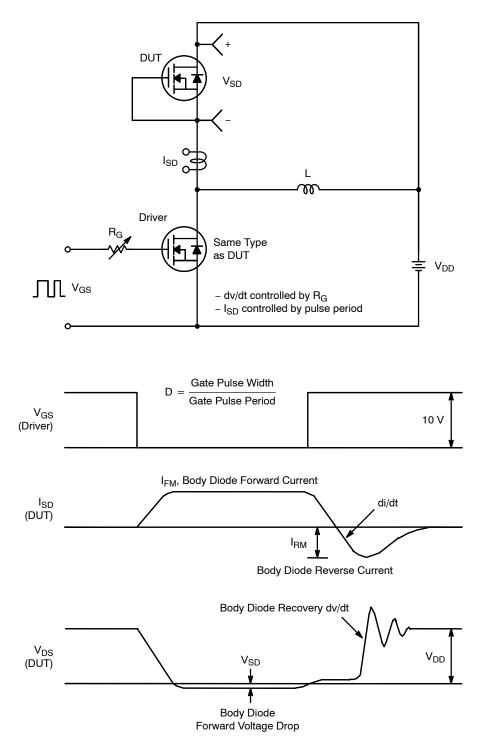
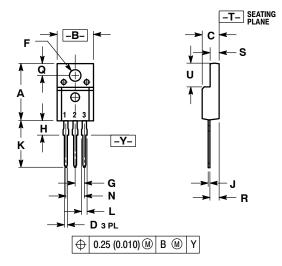


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

PACKAGE DIMENSIONS

TO-220 FULLPAK CASE 221D-03

ISSUE K



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: INCH 3. 221D-01 THRU 221D-02 OBSOLETE, NEW

STANDARD 221D-03.

	INCHES		MILLIN	METERS	
DIM	MIN MAX		MIN	MAX	
Α	0.617	0.635	15.67	16.12	
В	0.392	0.419	9.96	10.63	
С	0.177	0.193	4.50	4.90	
D	0.024	0.039	0.60	1.00	
F	0.116	0.129	2.95	3.28	
G	0.100 BSC		2.54 BSC		
Н	0.118	0.135	3.00	3.43	
J	0.018	0.025	0.45	0.63	
K	0.503	0.541	12.78	13.73	
L	0.048	0.058	1.23	1.47	
Ν	0.200 BSC		5.08 BSC		
Q	0.122	0.138	3.10	3.50	
R	0.099	0.117	2.51	2.96	
S	0.092	0.113	2.34	2.87	
U	0.239	0.271	6.06	6.88	

ON Semiconductor and ware trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Email Requests to: orderlit@onsemi.com

TECHNICAL SUPPORT

ON Semiconductor Website: www.onsemi.com

North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910 For additional information, please contact your local Sales Representative