# PNP Silicon General Purpose Amplifier Transistor

This PNP transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416/SC-90 package which is designed for low power surface mount applications, where board space is at a premium.

### Features

- Reduces Board Space
- High h<sub>FE</sub>, 210–460 (typical)
- Low V<sub>CE(sat)</sub>, < 0.5 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

### **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ )

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>(BR)CBO</sub>	-60	Vdc
Collector - Base Voltage	V <sub>(BR)CEO</sub>	-50	Vdc
Emitter – Base Voltage	V <sub>(BR)EBO</sub>	-6.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	-100	mAdc

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 1)	PD	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

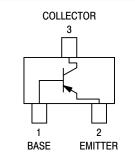


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CASE 463 STYLE 1



### MARKING DIAGRAM





- M = Date Code\*
- = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
2SA1774G	SC–75 (Pb–Free)	3,000/Tape & Reel
S2SA1774G	SC–75 (Pb–Free)	3,000/Tape & Reel
2SA1774T1G	SC–75 (Pb–Free)	3,000/Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

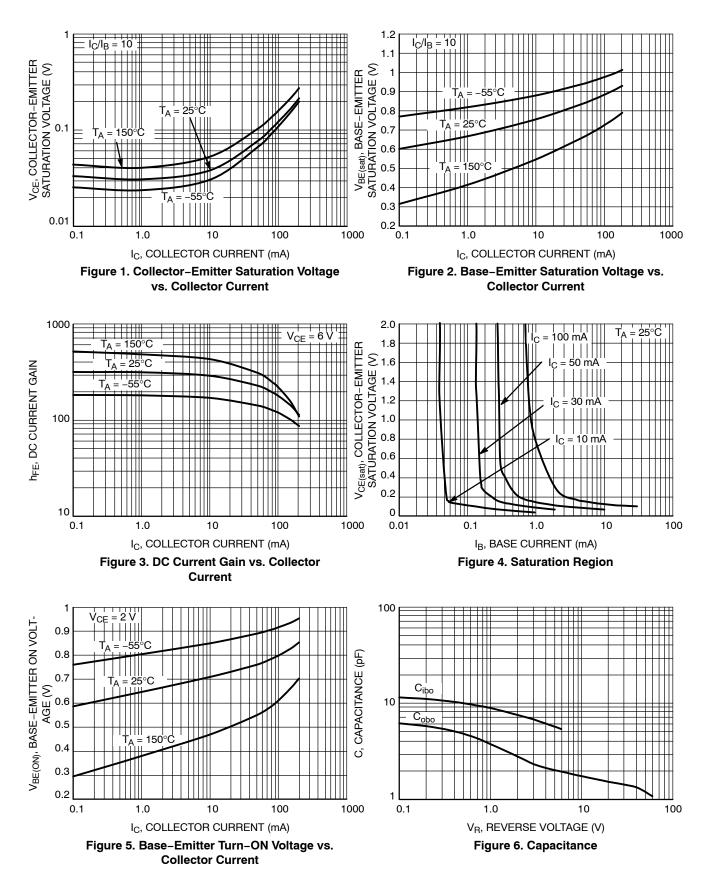
\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Characteristic	Symbol	Min	Тур	Max	Unit
Collector–Base Breakdown Voltage $(I_C = -50 \ \mu Adc, I_E = 0)$	V <sub>(BR)CBO</sub>	-60	_	_	V
Collector–Emitter Breakdown Voltage $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	-50	_	_	V
Emitter–Base Breakdown Voltage (I <sub>E</sub> = $-50 \ \mu$ Adc, I <sub>E</sub> = 0)	V <sub>(BR)EBO</sub>	-6.0	_	_	V
Collector–Base Cutoff Current ( $V_{CB} = -30$ Vdc, $I_E = 0$ )	I <sub>CBO</sub>	-	_	-0.5	μA
Emitter–Base Cutoff Current ( $V_{EB} = -5.0$ Vdc, $I_B = 0$ )	I <sub>EBO</sub>	_	_	-0.5	μA
Collector–Emitter Saturation Voltage (Note 2) $(I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc})$	V <sub>CE(sat)</sub>	_	_	-0.5	V
DC Current Gain (Note 2) $(V_{CE} = -6.0 \text{ Vdc}, I_C = -1.0 \text{ mAdc})$	h <sub>FE</sub>	120	_	560	_
Transition Frequency $(V_{CE} = -12 \text{ Vdc}, I_C = -2.0 \text{ mAdc}, f = 30 \text{ MHz})$	f <sub>T</sub>	_	140	_	MHz
Output Capacitance ( $V_{CB} = -12$ Vdc, $I_E = 0$ Adc, $f = 1$ MHz)	C <sub>OB</sub>	_	3.5	_	pF

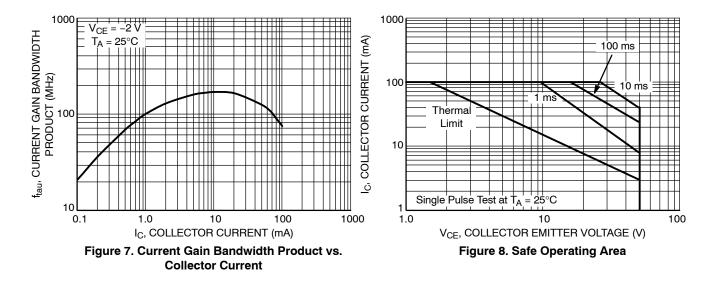
**ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

2. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, D.C.  $\leq$  2%.

### **TYPICAL ELECTRICAL CHARACTERISTICS**



### TYPICAL ELECTRICAL CHARACTERISTICS







\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

1.000

0.039

SCALE 10:1

mm

inches

0.508

0.020

 
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 DESCRIPTION:
 SC-75/SOT-416
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