MOSFET – Power, N-Channel, Silicon Carbide, TO-247-4L 1200 V, 80 mΩ



Description

Silicon Carbide (SiC) MOSFET uses a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operation frequency, increased power density, reduced EMI, and reduced system size.

Features

- 1200 V @ $T_J = 175$ °C
- Max $R_{DS(on)} = 110 \text{ m}\Omega$ at $V_{GS} = 20 \text{ V}$, $I_D = 20 \text{ A}$
- High Speed Switching with Low Capacitance
- 100% Avalanche Tested
- RoHS Compliant

Applications

- Industrial Motor Drive
- UPS
- Boost Inverter
- PV Charger

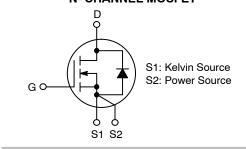


ON Semiconductor®

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V _{DSS}	R _{DS(ON)} TYP	I _D MAX
1200 V	80 mΩ	29 A

N-CHANNEL MOSFET





MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
ZZ = Lot Traceability
NTH4L080N120SC1 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$, unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V _{DSmax}	Drain-to-Source Voltage		1200	V
V_{GSmax}	Max. Gate-to-Source Voltage	@ T _C < 150°C	-15 / +25	V
V _{GSop} (DC)	Recommended operation Values of Gate – Source Voltage	@ T _C < 150°C	-5 / +20	V
V _{GSop} (AC)	Recommended operation Values of Gate – Source Voltage (f > 1 Hz)	@ T _C < 150°C	-5 / +20	V
I _D	Continuous Drain Current	V _{GS} = 20 V, T _C = 25°C	29	А
		V _{GS} = 20 V, T _C = 100°C	21	
I _{D(Pulse)}	Pulse Drain Current	Pulse width tp limited by Tj max	125	А
E _{AS}	Single Pulse Avalanche Energy (Note 1)	171	mJ	
P _{tot}	Power Dissipation	T _C = 25°C	170	W
		T _C = 150°C	28	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. E_{AS} of 171 mJ is based on starting Tj = 25°C, L = 1 mH, I_{AS} = 18.5 A, , V_{DD} = 50 V, R_{G} = 25 Ω .

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case	0.88	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	40	

PACKAGE MARKING AND ORDERING INFORMATION

Part	Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4L0	80N120SC1	NTH4L080N120SC1	TO-247-4L	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions		Min	Тур	Max	Unit
OFF CHARACT	ERISTICS				•		
BV _{DSS}	Drain-to-Source Breakdown Voltage	$I_D = 100 \mu A, V_{GS} = 0 V$		1200	_	-	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 5 mA, Referenced to 25°C		-	0.3	_	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 1200 V, V _{GS} = 0	$V \begin{array}{c} T_C = 25^{\circ}C \\ T_C = 150^{\circ}C \end{array}$	-	- -	100 1.0	μA mA
I _{GSS}	Gate-to-Source Leakage Current	V _{GS} = 25 V, V _{DS} = 0 V		-	-	1	μΑ
I _{GSSR}	Gate-to-Source Leakage Current, Reverse	$V_{GS} = -15 \text{ V}, V_{DS} = 0 \text{ V}$	/	-	_	-1	μΑ
ON CHARACTE	RISTICS						
V _{GS(th)}	Gate-to-Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 5 \text{ mA}$		1.8	2.75	4.3	V
R _{DS(on)}	Static Drain-to-Source On Resistance	$V_{GS} = 20 \text{ V}, I_D = 20 \text{ A}$		_	80	110	mΩ
		$V_{GS} = 20 \text{ V}, I_D = 20 \text{ A},$	T _C = 150°C	_	127	162	
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 20 A		-	11.3	-	S
		V _{DS} = 20 V, I _D = 20 A,	T _C = 150°C	-	9.8	-	1
DYNAMIC CHA	RACTERISTICS				1	ı	1
C _{iss}	Input Capacitance	V _{DS} = 800 V, V _{GS} = 0 V	V, f = 1 MHz	-	1112	1670	pF
C _{oss}	Output Capacitance			-	80	120	pF
C _{rss}	Reverse Transfer Capacitance			-	6.5	10	pF
E _{oss}	C _{oss} Stored Energy			_	32	_	μJ
	IARACTERISTICS				I	I	<u> </u>
t _{d(on)}	Turn-On Delay Time	V _{CC} = 800 V, I _C = 20 A	,	-	9	18	ns
t _r	Rise Time	$V_{GS}^{-} = -5/20 \text{ V, } R_G = 4.7 \Omega$ Inductive Load, $T_C = 25^{\circ}\text{C}$		-	4.2	10	ns
t _{d(off)}	Turn-Off Delay Time			-	26.8	43	ns
t _f	Fall Time			-	5.4	11	ns
E _{on}	Turn-on Switching Loss			-	314	-	μJ
E _{off}	Turn-off Switching Loss			-	32	_	μJ
E _{ts}	Total Switching Loss			-	346	_	μJ
Qg	Total Gate Charge	V _{DD} = 600 V, I _D = 20 A		_	56	_	nC
Q _{gs}	Gate-to-Source Charge	$V_{GS} = -5/20 \text{ V}$		_	11	_	nC
Q _{gd}	Gate-to-Drain Charge			_	12	_	nC
R _G	Gate input resistance	f = 1 MHz, D-S short		_	1.7	_	Ω
DIODE CHARA	CTERISTICS				ı		<u> </u>
V _{SD}	Source-to-Drain Diode Forward	$V_{GS} = -5 V$,	T _C = 25°C	=	3.7	-	V
	Voltage	I _{SD} = 10 A	T _C = 150°C	=	3.3	-	1
E _{rec}	Reverse Recovery Energy	I _{SD} = 20 A,	T _C = 150°C	_	29	-	μJ
t _{rr}	Diode Reverse Recovery Time	$V_{GS} = -5 \text{ V},$ $V_{R} = 600 \text{ V},$	T _C = 25°C	_	18	-	ns
		dl _{SD} /dt = 1000 A/μs	T _C = 150°C	_	31	-	1
Q _{rr}	Diode Reverse Recovery Charge	† †	T _C = 25°C	_	80	_	nC
			T _C = 150°C	_	212	_	
I _{rrm}	Peak Reverse Recovery Current		T _C = 25°C	_	9	_	Α
	ĺ		T _C = 150°C	_	14	_	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS T_J = 25°C unless otherwise noted

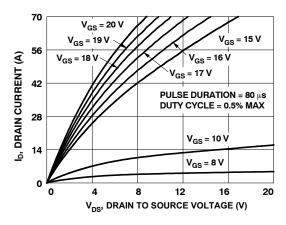


Figure 1. On Region Characteristics

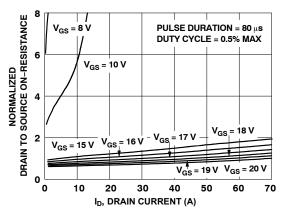


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

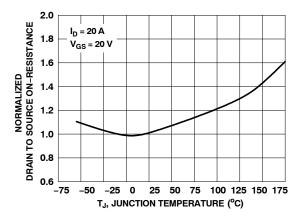


Figure 3. Normalized On Resistance vs. Junction Temperature

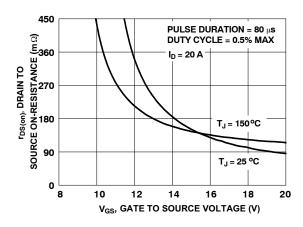


Figure 4. On-Resistance vs. Gate-to-Source Voltage

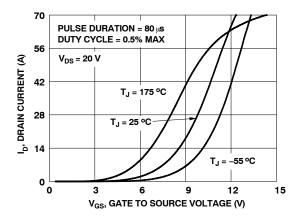


Figure 5. Transfer Characteristics

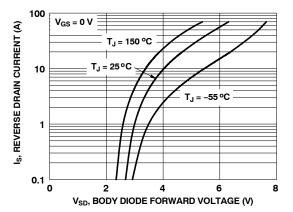


Figure 6. Source-to-Drain Diode Forward Voltage vs. Source Current

TYPICAL CHARACTERISTICS $T_J = 25^{\circ}C$ unless otherwise noted

10000

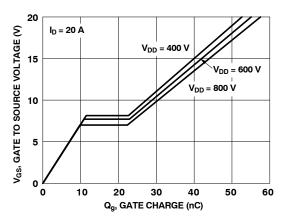


Figure 7. Gate Charge Characteristics

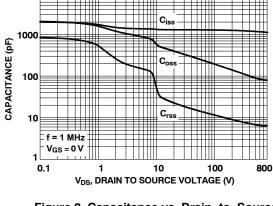


Figure 8. Capacitance vs. Drain-to-Source Voltage

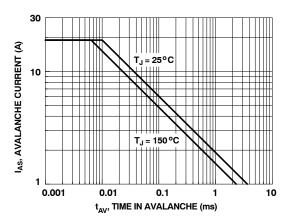


Figure 9. Unclamped Inductive Switching Capability

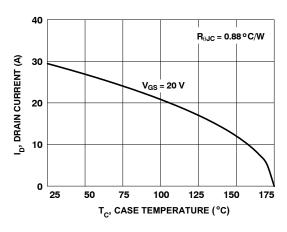


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

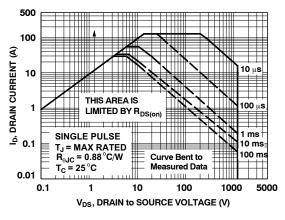


Figure 11. Forward Bias Safe Operating Area

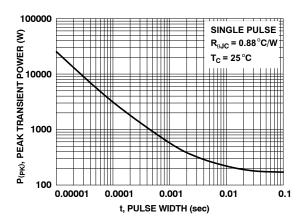


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS $T_J = 25^{\circ}C$ unless otherwise noted

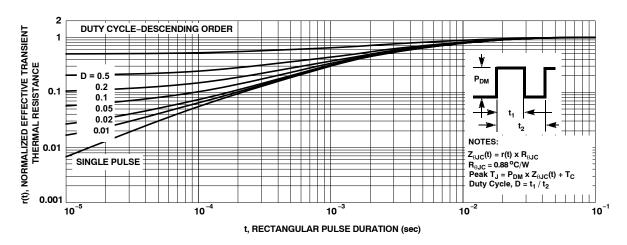
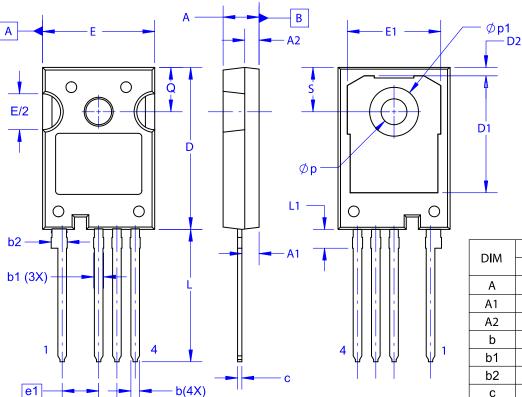


Figure 13. Junction-to-Case Transient Thermal Response Curve

TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019



NOTES:

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 B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
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 D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MIN	NOM	MAX	
A	4.80	5.00	5.20	
A1	2.10	2.40	2.70	
A2	1.80	2.00	2.20	
b	1.07	1.20	1.33	
b1	1.20	1.40	1.60	
b2	2.02	2.22	2.42	
С	0.50	0.60	0.70	
D	22.34	22.54	22.74	
D1	16.00	16.25	16.50	
D2	0.97	1.17	1.37	
е	2.54 BSC			
e1	5.08 BSC			
E	15.40	15.60	15.80	
E1	12.80	13.00	13.20	
E/2	4.80	5.00	5.20	
L	18.22	18.42	18.62	
L1	2.42	2.62	2.82	
р	3.40	3.60	3.80	
p1	6.60	6.80	7.00	
Q	5.97	6.17	6.37	
S	5.97	6.17	6.37	

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