

Silicon Carbide (SiC) **MOSFET** - 12 mohm, 650 V, M2, TO-247-4L

NTH4L015N065SC1

Features

- Typ. $R_{DS(on)} = 12 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$ Typ. $R_{DS(on)} = 15 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 283 \text{ nC})$
- High Speed Switching with Low Capacitance ($C_{oss} = 430 \text{ pF}$)
- 100% Avalanche Tested
- $T_I = 175^{\circ}C$
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

Typical Applications

- SMPS (Switching Mode Power Supplies)
- Solar Inverters
- UPS (Uninterruptable Power Supplies)
- Energy Storages

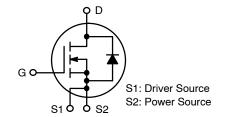
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	650	V
Gate-to-Source Voltage)		V_{GS}	-8/+22	٧
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-5/+18	>	
Continuous Drain Current (Note 1)	Steady State	T _C = 25°C	I _D	142	Α
Power Dissipation (Note 1)			P _D	500	W
Continuous Drain Current (Note 1)	Steady State	T _C = 100°C	I _D	100	Α
Power Dissipation (Note 1)			P _D	250	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	483	Α
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	
Source Current (Body Diode)			I _S	114	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 13 A, L = 1 mH) (Note 3)			E _{AS}	84	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. EAS of 84 mJ is based on starting $T_J = 25^{\circ}C$; L = 1 mH, $I_{AS} = 13 \text{ A}$, $V_{DD} = 50 \text{ V}, V_{GS} = 18 \text{ V}.$

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	18 mΩ @ 18 V	142 A

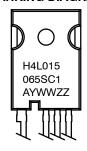


N-CHANNEL MOSFET



CASE 340CJ

MARKING DIAGRAM



H4L015065SC1 = Specific Device Code

= Assembly Location

= Year

WW = Work Week = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NTH4L015N065SC1	TO247-4L	30 Units / Tube

Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.3	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	40	

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		650	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 20 mA, referenced to 25°C		-	0.12	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C	-	_	10	μΑ
		V _{DS} = 650 V	T _J = 175°C	-	-	1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +22/-8 \text{ V}, V_{DS}$	s = 0 V	-	_	250	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D} = 25 \text{ m}$	ıA	1.8	2.5	4.3	V
Recommended Gate Voltage	V_{GOP}			-5	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 75 A	, T _J = 25°C	-	15	-	mΩ
		V _{GS} = 18 V, I _D = 75 A	, T _J = 25°C	-	12	18	
		V _{GS} = 18 V, I _D = 75 A, T _J = 175°C		-	16	-	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 75 A	L	-	47	-	S
CHARGES, CAPACITANCES & GATE RE	SISTANCE						
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz, } V_{DS} = 325 \text{ V}$		_	4790	-	pF
Output Capacitance	C _{OSS}			-	430	-	
Reverse Transfer Capacitance	C _{RSS}			-	33	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_D = 75 \text{ A}$ $f = 1 \text{ MHz}$		-	283	-	nC
Gate-to-Source Charge	Q _{GS}			_	72	-	
Gate-to-Drain Charge	Q_{GD}			-	64	-	
Gate-Resistance	R _G			-	1.6	-	Ω
SWITCHING CHARACTERISTICS	•						
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/18 \text{ V}, V_{DS} =$	400 V,	-	23	-	ns
Rise Time	t _r	I_D = 75 A, R_G = 2.2 Ω Inductive load	?	-	26	-	
Turn-Off Delay Time	t _{d(OFF)}	1		-	49	-	
Fall Time	t _f	1		_	9.6	-	
Turn-On Switching Loss	E _{ON}	1		-	167	-	μJ
Turn-Off Switching Loss	E _{OFF}	-		-	276	-	
Total Switching Loss	E _{tot}			_	443	_	
SOURCE-DRAIN DIODE CHARACTERIS							
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V}, T_J = 25^{\circ} \text{ C}$	С	-	-	114	Α
Pulsed Source–Drain Diode Forward Current (Note 2)	I _{SDM}			-	-	483	
	V _{SD}	!	A, T _J = 25°C		 		V

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
SOURCE-DRAIN DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/18 \text{ V}, I_{SD} = 75 \text{ A},$	-	28	-	ns	
Reverse Recovery Charge	Q _{RR}	$dI_S/dt = 1000 \text{ A/}\mu\text{s}$	-	234	-	nC	
Reverse Recovery Energy	E _{REC}		-	23	-	μJ	
Peak Reverse Recovery Current	I _{RRM}		-	16	-	Α	
Charge Time	Ta		-	17	-	ns	
Discharge Time	Tb		-	11	-	ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

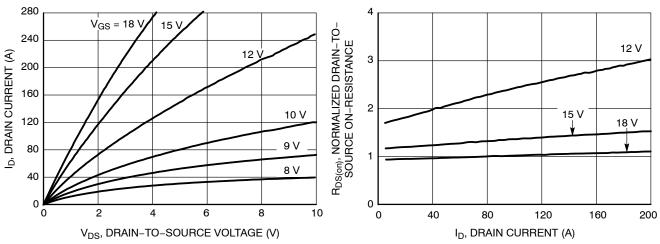


Figure 1. On-Region Characteristics

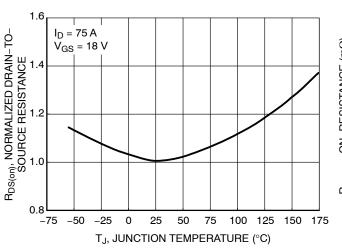


Figure 3. On–Resistance Variation with Temperature

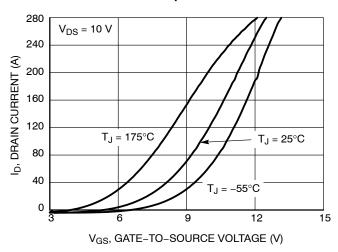


Figure 5. Transfer Characteristics



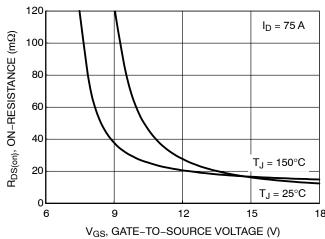
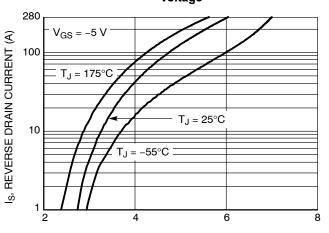


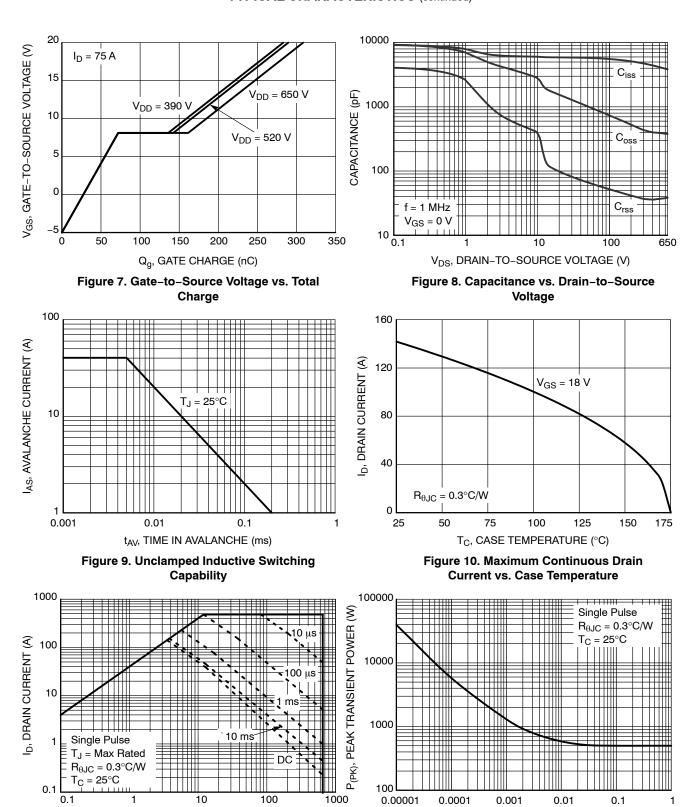
Figure 4. On-Resistance vs. Gate-to-Source Voltage



V_{SD}, BODY DIODE FORWARD VOLTAGE (V)

Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS (continued)



t, PULSE WIDTH (sec)

Figure 12. Single Pulse Maximum Power Dissipation

V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

Figure 11. Safe Operating Area

TYPICAL CHARACTERISTICS (continued)

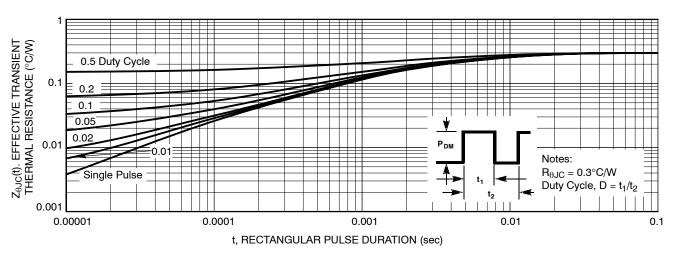
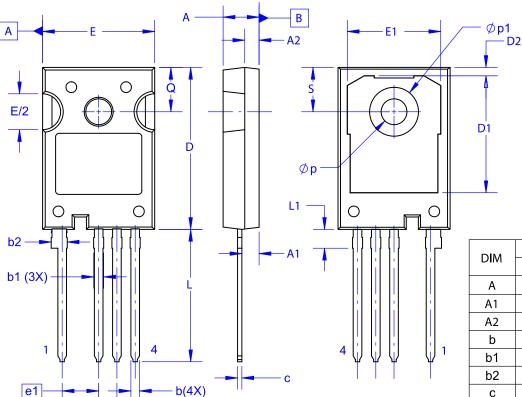


Figure 13. Junction-to-Case Thermal Response

TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019



NOTES:

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DIM	MIN	NOM	MAX		
A	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5.08 BSC				
E	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42	2.62	2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

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