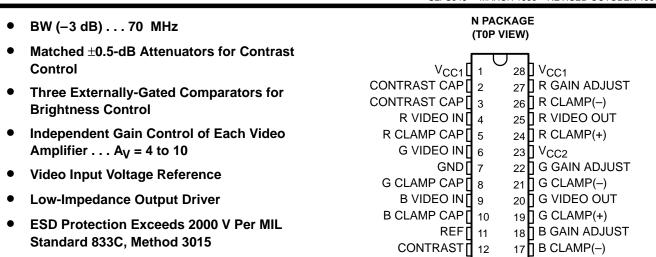
SLFS048 - MARCH 1990 - REVISED OCTOBER 1991

16 B VIDEO OUT

15 B CLAMP(+)



description

The LM1203 and LM1203A are wide-band video amplifier systems intended for high-resolution RGB color monitor applications. In addition to three matched video amplifiers, the LM1203 and LM1203A contain three gated differential input black-level clamp comparators for brightness control and three matched attenuator circuits for contrast control. Each video amplifier contains a gain set for adjusting maximum system gain ($A_V = 4$ to 10) as well as providing trim capability. The LM1203 and LM1203A also contain a voltage reference for the video inputs.

V_{CC1}

CLAMP GATE

13

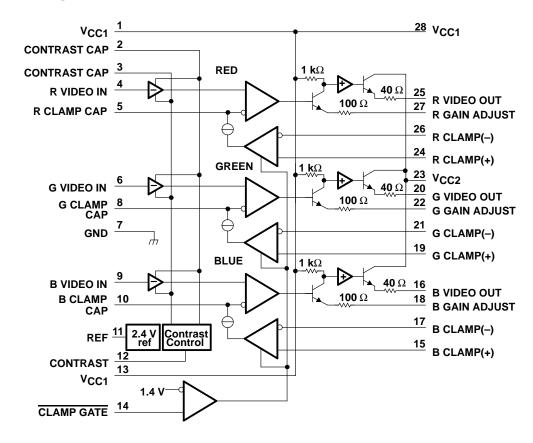
The LM1203 and LM1203A are characterized for operation from 0°C to 70°C.

Designed to Be an Improved Replacement

for National Semiconductor LM1203

1

functional block diagram



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	13.5 V
Input voltage range, V _I	$V_{CC} \ge V_I \ge GND$
Video output current	28 mA
Total power dissipation at (or below) 25°C free-air temperature (see Note 2)	2.5 W
Operating junction temperature	150°C
Operating free-air temperature range	0°C to 70°C
Storage temperature range	65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

NOTES: 1. All V_{CC} pins must be externally wired together to prevent internal damage during V_{CC} power-on/off cycles.

2. For operating above 25°C free-air temperature, derate linearly at the rate of 20 mW/°C.



electrical characteristics at 25°C free-air temperature, V_{12} = 6 V, V_{14} = 0, V_{15} = 2 V, = V_{CC2} = 12 V (see Figure 1) (unless otherwise noted)

V_{CC1}

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Icc	Supply current	V _{CC1} only		60	90	mA
V _{ref}	Video input reference voltage		2.2	2.4	2.6	V
I _{IB}	Video input bias current	Any amplifier		5	20	μΑ
VIL(14)	Clamp gate low-level input voltage	Clamp comparators on	0.8	1.2		V
VIH(14)	Clamp gate high-level input voltage	Clamp comparators off		1.6	2	V
I _{IL} (14)	Clamp gate low input current	V ₁₄ = 0		-0.5	-5	μΑ
^I IH(14)	Clamp gate high input current	V ₁₄ = V _{CC}		0.005	1	μΑ
I _{K(chg)}	Clamp capacitor charge current	$V_{5,8 \text{ or } 10} = 0$		850		μΑ
IK(dschg)	Clamp capacitor discharge current	V _{5,8 or 10} = 5 V		-850		μΑ
VOL	Low-level output voltage	$V_{5,8 \text{ or } 10} = 0$		1.2		V
VOH	High-level output voltage	$V_{5,8 \text{ or } 10} = 5 \text{ V}$		8.9		V
VOdiff	Output voltage difference between any two channels	V ₁₅ = 2 V	±0.5		±50	mV
		V ₁₅ = 4 V			±30	

operating characteristics at 25°C free-air temperature, V_{14} = 0 V, V_{15} = 4 V, f_{in} = 10 kHz (unless otherwise noted)

PARAMETER		TEST CONDITIONS			MIN TYP	MAX	UNIT	
A _{Vmax}	Maximum voltage amplification	V ₁₂ = 12 V,	V _{I(PP)} = 560 mV	LM1203	8.8		V/V	
				LM1203A	7.8		V/V	
AVmid	Mid-range voltage amplification	V ₁₂ = 5 V,	V _{I(PP)} = 560 mV	LM1203	3.5		V/V	
				LM1203A	2.5		V/V	
V _{12low}	Contrast voltage for minimum amplification	V _{I(PP)} = 1 V,	See Note 3		2		V	
AVmax(diff)	Amplification match at A _V max	$V_{12} = 12 \text{ V},$	See Note 4		±0.2		dB	
AVmid(diff)	Amplification match at Ay mid	$V_{12} = 5 V$,	See Note 3		±0.2		dB	
AVlow(diff)	Amplification match at Ay low	$V_{12} = V_{12low}$	See Notes 3 and 4		±0.2		dB	
THD	Total harmonic distortion	$V_{12} = 3 V$,	V _{I(PP)} = 1 V		0.5		%	
BW(-3 dB)	Amplifier bandwidth	$V_{12} = 12 \text{ V},$	See Notes 5 and 7		70		MHz	
a _X	Crosstalk attenuation	$V_{12} = 12 \text{ V},$	f = 10 kHz,	See Note 6	60		dB	
		V ₁₂ = 12 V,	f = 10 MHz,		40			
		See Notes 6 an	d 7					

- NOTES: 3. Determine V_{12low} for -40-dB attenuation of output. Reference to AV maximum.
 - 4. Measure gain difference between any two amplifiers, $V_{I(PP)} = 1 \text{ V}$.
 - Adjust input frequency from 10 kHz (Ay maximum ref level) to the -3-dB corner frequency (f -3 dB). V_I(PP) = 560 mV.
 - V_{I(PP)} = 560 mV at f = 10 kHz to any amplifier. Measure output levels of the other two undriven amplifiers relative to driven amplifier to determine channel separation. Terminate the undriven amplifier inputs to simulate generator loading. Repeat test at f = 10 MHz for a_X = 10 MHz.
 - 7. A special text fixture without a socket is required.



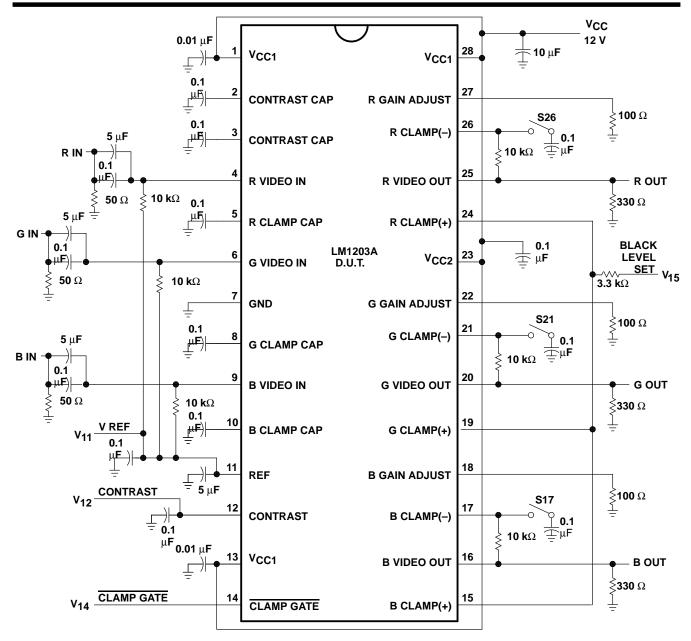


Figure 1. Test Circuit

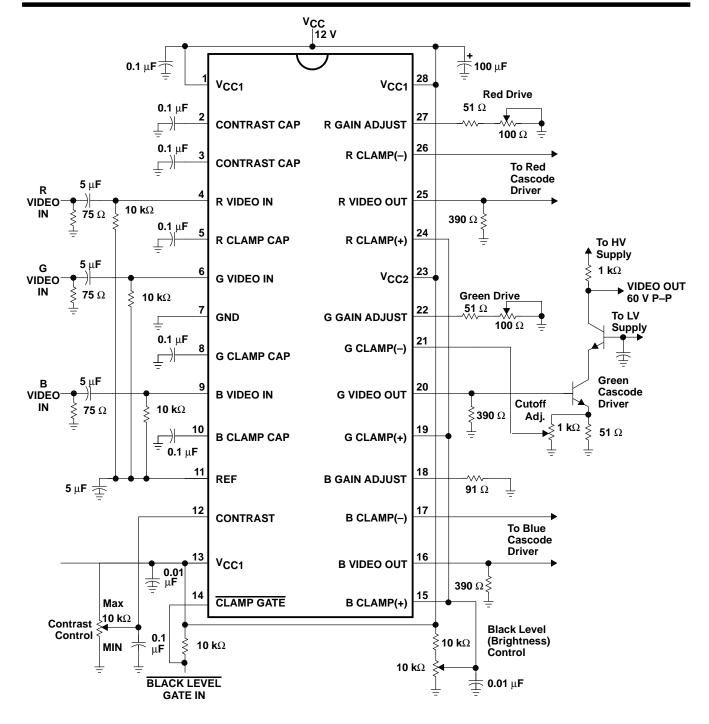


Figure 2. Typical Application

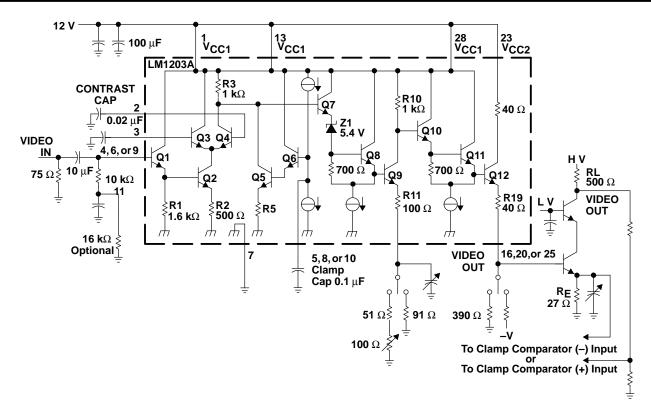


Figure 3. Simplified Video Amplifier Section With Recommended External Components

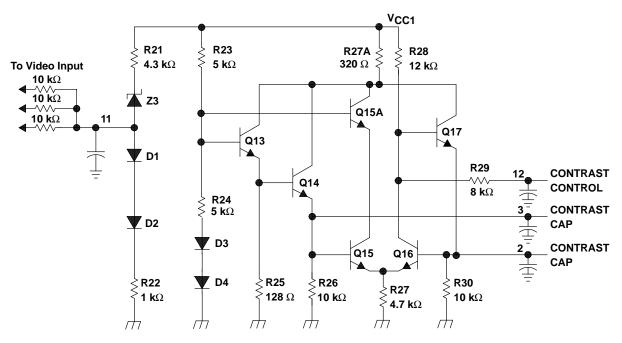


Figure 4. Input Voltage Reference and Contrast Control Circuits



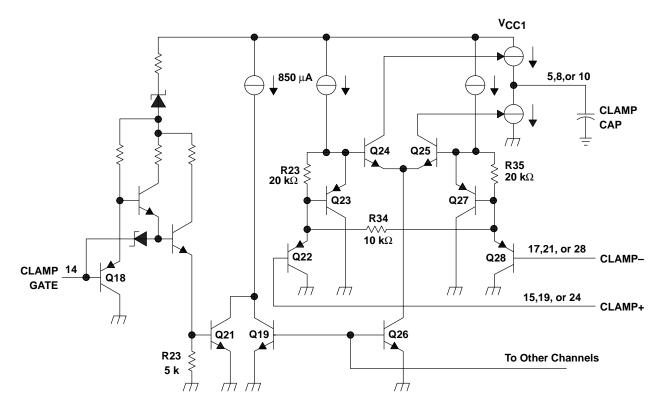


Figure 5. Simplified Schematic of LM1203A Clamp Gate (Common to Each Channel) and Clamp Comparator Circuits

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