SDFS038A - D2932, MARCH 1987 - REVISED OCTOBER 1993

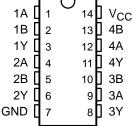
 Package Options Include Plastic Small-Outline Packages and Standard Plastic 300-mil DIPs

description

The SN74F08 contains four independent 2-input AND gates. It <u>performs</u> the Boolean functions $Y = A \cdot B$ or $Y = \overline{A} + \overline{B}$ in positive logic.

The SN74F08 is characterized for operation from 0°C to 70°C.

D OR N PACKAGE (TOP VIEW)

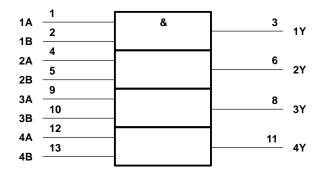


FUNCTION TABLE (each gate)

INPUTS		OUTPUT			
Α	В	Υ			
Н	Н	Н			
L	X	L			
Х	L	L			

logic symbol†

logic diagram, each gate (positive logic)





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	–1.2 V to 7 V
Input current range	30 mA to 5 mA
Voltage range applied to any output in the high state	0.5 V to V _{CC}
Current into any output in the low state	40 mA
Operating free-air temperature range	0°C to 70°C
Storage temperature range	65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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recommended operating conditions

		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	V
٧ıH	High-level input voltage	2			V
VIL	Low-level input voltage			0.8	V
liK	Input clamp current			-18	mA
IOH	High-level output current			- 1	mA
loL	Low-level output current			20	mA
TA	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	1	TEST CONDITIONS			MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	$I_1 = -18 \text{ mA}$			-1.2	V
VOH	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -1 \text{ mA}$	2.5	3.4		V
	$V_{CC} = 4.75 V$,	$I_{OH} = -1 \text{ mA}$	2.7			V
V _{OL}	$V_{CC} = 4.5 V,$	$I_{OL} = 20 \text{ mA}$		0.3	0.5	V
Ι _Ι	$V_{CC} = 5.5 V,$	V _I = 7 V			0.1	mA
IIH	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20	μΑ
I _{IL}	$V_{CC} = 5.5 V$,	V _I = 0.5 V			- 0.6	mA
los [‡]	$V_{CC} = 5.5 V$,	V _O = 0	-60		-150	mA
ГССН	$V_{CC} = 5.5 V$,	V _I = 4.5 V		5.5	8.3	mA
l _{CCL}	$V_{CC} = 5.5 \text{ V},$	$V_I = 0$		8.6	12.9	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$C_L = 50 pF$		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = \text{MIN to MAX} \$$		UNIT	
			MIN	TYP	MAX	MIN	MAX	
^t PLH	A or B	V	2.2	3.8	5.6	2.2	6.6	
^t PHL		ī	1.7	3.6	5.3	1.7	6.3	ns

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and waveforms are shown in Section 1.



[‡] Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

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