RoHS

COMPLIANT HALOGEN

FREE



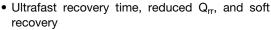
### Vishay Semiconductors

# Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRODUCT SUMMARY				
Package	TO-263AC (SMPD)			
I <sub>F(AV)</sub>	2 x 15 A			
$V_R$	600 V			
V <sub>F</sub> at I <sub>F</sub>	0.9 V			
t <sub>rr</sub>	55 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Dual die			

#### **FEATURES**





- For PFC CRM, snubber operation
- Low forward voltage drop
- Low leakage current

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		$V_{RRM}$		600	V
A	per device	I <sub>F(AV)</sub>	T <sub>solder pad</sub> = 143 °C	30	
Average rectified forward current	per diode			15	۸
	per device		T <sub>J</sub> = 25 °C, 6 ms square pulse	300	Α
Non-repetitive peak surge current	per diode	IFSM		160	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	
Forward voltage, per diode V <sub>F</sub>	V	I <sub>F</sub> = 15 A	-	1.03	1.25	V
	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.9	1.1		
Reverse leakage current, per diode	I <sub>R</sub>	$V_R = V_R$ rated	-	-	15	
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	70	300	- μΑ
Junction capacitance, per diode	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	13	-	pF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I <sub>F</sub> = 1 A, dI <sub>F</sub> /dt = 50 A	Vμs, V <sub>R</sub> = 30 V	-	55	-	
Reverse recovery time		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	65	
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt = 500 A/μs, V <sub>R</sub> = 400 V	-	96	-	ns
		T <sub>J</sub> = 125 °C		-	150	-	
Peak recovery current		T <sub>J</sub> = 25 °C		-	18	-	^
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	26	-	Α
Deviates vession shows	0	T <sub>J</sub> = 25 °C		-	1.0	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	2.0	-	μC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	+175	°C
Thermal resistance, per diode junction to solder pad	R <sub>thJ-Sp</sub>		-	1.2	1.7	°C/W
Approximate weight				0.55		g
Approximate weight				0.02		oz.
Marking device		Case style TO-263AC (SMPD)	30CDU06			

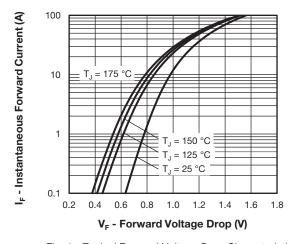


Fig. 1 - Typical Forward Voltage Drop Characteristics

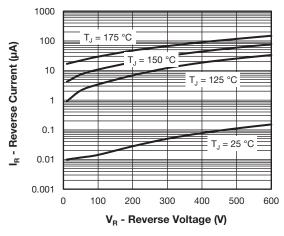


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

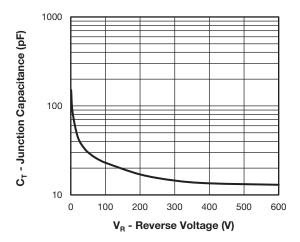


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

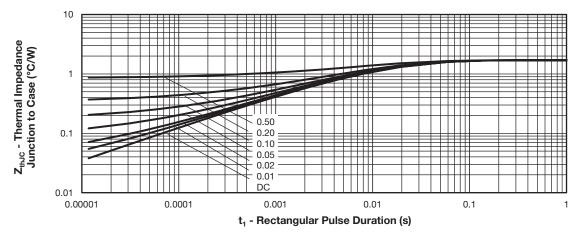


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

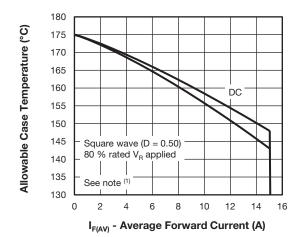


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

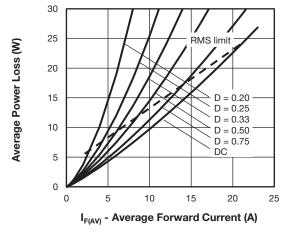


Fig. 6 - Forward Power Loss Characteristics

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = rated V_R$ 



#### www.vishay.com

## Vishay Semiconductors

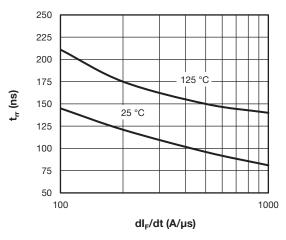


Fig. 7 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

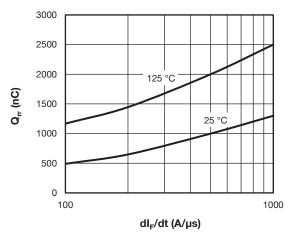
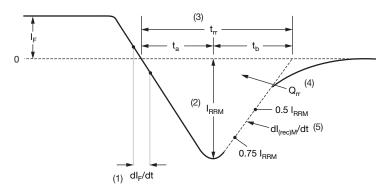


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

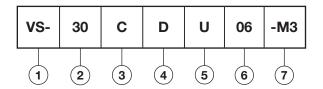
(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Current rating (30 A)

3 - Circuit configuration:

C = common cathode

- D = SMPD package

5 - Process type,

U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

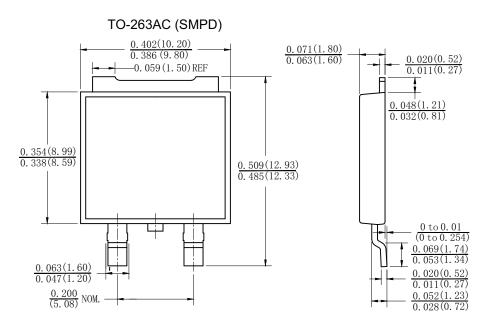
ORDERING INFORMATION (Example)						
PREFERRED P/N	ERRED P/N QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30CDU06-M3/I	2000	2000	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95604</u>				
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			

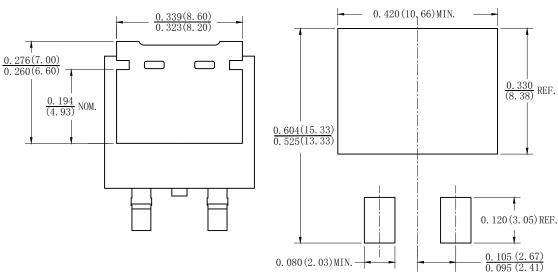


# TO-263AC (SMPD)

### **DIMENSIONS** in inches (millimeters)



### Mounting Pad Layout





### **Legal Disclaimer Notice**

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