Vishay Semiconductors

Thyristor High Voltage, Phase Control SCR, 40 A



PRIMARY CHARACTERISTICS				
I _{T(AV)}	35 A			
V _{DRM} /V _{RRM}	1200 V			
V_{TM}	1.45 V			
I _{GT}	150 mA			
T_J	-40 °C to +125 °C			
Package	TO-247AD 3L			
Circuit configuration	Single SCR			

FEATURES

- Low I_{GT} parts available
- Designed and qualified according to JEDEC® - JESD 47



- Flexible solution for reliable AC power rectification
- Easy control peak current at charger power up to reduce passive / electromechanical components
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding and battery charge

DESCRIPTION

The VS-40TPS12.. high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications.

AEC-Q101 qualified P/N available (VS-40TPS12LHM3, VS-40TPS12ALHM3).

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	35	A		
I _{RMS}		55	^		
V _{RRM} /V _{DRM}		1200	V		
I _{TSM}		600	А		
V_{T}	40 A, T _J = 25 °C	1.45	V		
dv/dt		1000	V/µs		
di/dt		100	A/µs		
T _J		-40 to +125	°C		

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA
VS-40TPS12AL-M3	1200	1300	10
VS-40TPS12L-M3	1200	1300] 10



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ABSOLUTE MAXIMUM RATINGS	}				
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° conduction half sine wave	Э	35	
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}			55	А
Maximum peak, one-cycle		10 ms sine pulse, rated V _{RRM} applied		500	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	ladial	600	
Maximum I ² t for fusing	I ² t	10 ms sine pulse, rated V _{RRM} applied	Initial $T_{.1} = T_{.1} \text{ max.}$	1250	A ² s
Maximum I-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	ij – ijiliax.	1760	A-S
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied		17 600	A²√s
Low level value of threshold voltage	V _{T(TO)1}		1.02	V	
High level value of threshold voltage	V _{T(TO)2}	T 105 °C	1.23	v	
Low level value of on-state slope resistance	r _{t1}	T _J = 125 °C	9.74	m0	
High level value of on-state slope resistance	r _{t2}		7.50	mΩ	
Maximum peak on-state voltage	V_{TM}	110 A, T _J = 25 °C		1.85	V
Maximum rate of rise of turned-on current	di/dt	T _J = 25 °C		100	A/µs
Maximum holding current	I _H	Anode supply = 6 V , resistive load, initial T_J	= 1 A, I _T = 25 °C	300	
Maximum latching current	ΙL	Anode supply = 6 V, resistive load, $T_J = 25$	°C	350	т Л
Marrian was a sound alive at leading a summer		T _J = 25 °C	25 °C		mA
Maximum reverse and direct leakage current	I _{RRM/} I _{DRM}	$V_R = \text{rated } V_{RRM}/V_{DR}$	RM	10	1
Maximum rate of rise of off-state voltage 40TPS12A	dv/dt	$T_J = T_J$ maximum, linear to 80 % V_{DRM},R_g - k = 100 Ω		500	V/uo
Maximum rate of rise of off-state voltage 40TPS12	αν/ατ			1000 V/	V/µs

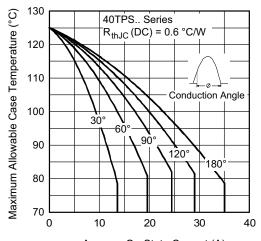
TRIGGERING					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum peak gate power	P _{GM}				W
Maximum average gate power	P _{G(AV)}			2.5	VV
Maximum peak gate current	I _{GM}			2.5	Α
Maximum peak negative gate voltage	-V _{GM}			10	V
		T _J = -40 °C	Anada sunah. CV	2.0	
Maximum required DC gate voltage to trigger	V_{GT}	T _J = 25 °C	Anode supply = 6 V	1.7	V
		T _J = 125 °C	Tesistive load	1.3	
		T _J = -40 °C	Anode supply = 6 V resistive load	200	
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C		150	mA
Maximum required DC gate current to trigger		T _J = 125 °C		80	
		$T_J = 25$ °C, for 40TPS12A		40	
Maximum DC gate voltage not to trigger for 40TPS12	V_{GD}	T _J = 125 °C, V _{DRM} = rated value		0.25	٧
Maximum DC gate current not to trigger for 40TPS12	I _{GD}			6	mA
Maximum DC gate voltage not to trigger for 40TPS12A	V_{GD}	T _J = 125 °C, V _{DRM} = rated value		0.15	V
Maximum DC gate current not to trigger for 40TPS12A	I _{GD}			1	mA



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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	e T _J , T _{Stg}		-40 to +125	°C			
Maximum thermal resistance, junction to case	R _{thJC}	DC appration	0.6				
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	40	°C/W			
Maximum thermal resistance, case to heat sink	R _{thCS}	Mounting surface, smooth and greased	0.25				
Approximate weight			6	g			
Approximate weight			0.21	oz.			
Mounting torque minimur	m		6 (5)	kgf · cm			
maximur	n		12 (10)	(lbf·in)			
Marking device		Case style TO-247AD 3L	40TPS12AL				
iviarking device		Case style 10-247AD 3L	40TPS12L				



Average On-State Current (A) Fig. 1 - Current Rating Characteristics

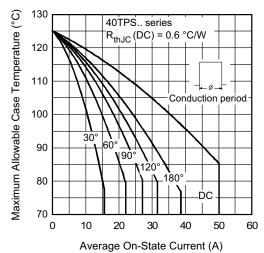


Fig. 2 - Current Rating Characteristics

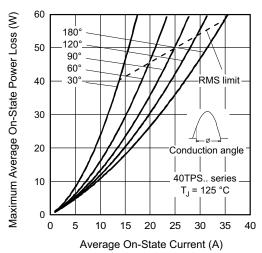


Fig. 3 - On-State Power Loss Characteristics

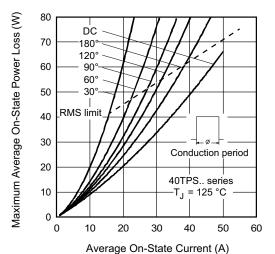


Fig. 4 - On-State Power Loss Characteristics

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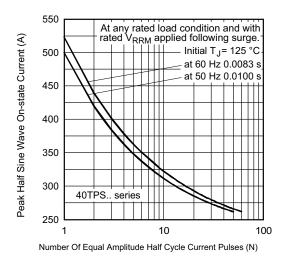


Fig. 5 - Maximum Non-Repetitive Surge Current

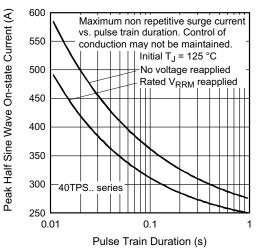
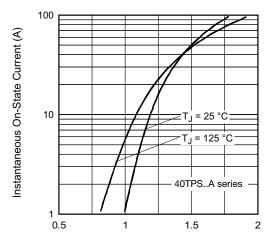
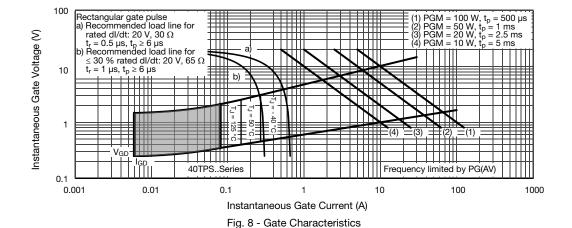


Fig. 6 - Maximum Non-Repetitive Surge Current



Instantaneous On-State Voltage (V)
Fig. 7 - On-State Voltage Drop Characteristics



Revision: 31-Jul-2018 4 Document Number: 95993

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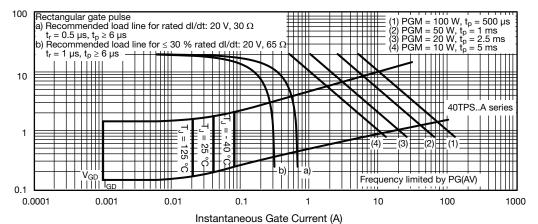


Fig. 9 - Gate Characteristics

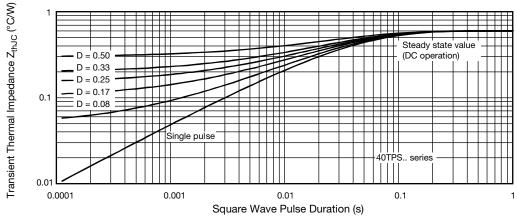


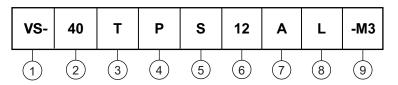
Fig. 10 - Thermal Impedance Z_{thJC} Characteristics

12 = 1200 V

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Circuit configuration:

T = thyristor

4 - Package:

P = TO-247

5 - Type of silicon:

S = standard recovery rectifier

6 - Voltage ratings

• A = Low lgt selection 40 mA maximum

• None = standard lgt selection

8 - L = long leads

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-40TPS12AL-M3	25	500	Antistatic plastic tubes		
VS-40TPS12L-M3	25	500	Antistatic plastic tubes		

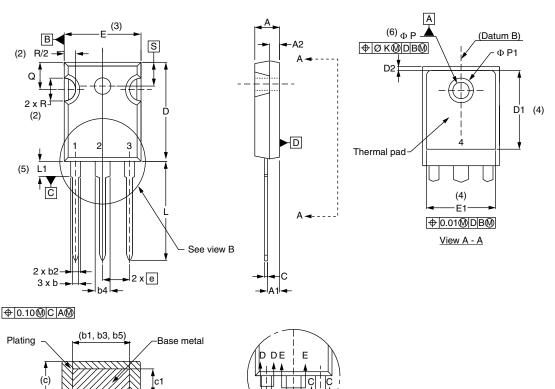
LINKS TO RELATED DOCUMENTS			
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626	
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007	



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TO-247AD 3L

DIMENSIONS in millimeters and inches



(4) Section C - C, D - D, E - E							
SYMBOL	MILLIN	IETERS	INC	HES	NOTES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.65	5.31	0.183	0.209			
A1	2.21	2.59	0.087	0.102			
A2	1.50	2.49	0.059	0.098			
b	0.99	1.40	0.039	0.055			

0.039

0.065

0.065

0.102

0.102

0.015

0.015

0.776

0.515

0.053

0.094

0.092

0.135

0.133

0.035

0.033

0.815

(h h2 h4)

:5	

View B

SYMBOL	IVIILLIIV	ILILING	INOTIES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØΚ	0.2	254	0.0	10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	
•	•		•		•

INCHES

MILLIMETERS

Notes

b1

b2

b3

b4

b5

С

с1

D

D1

(1) Dimensioning and tolerancing per ASME Y14.5M-1994

1.35

2.39

2.34

3.43

3.38

0.89

0.84

20.70

- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

3

- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1

0.99

1.65

1.65

2.59

2.59

0.38

0.38

19.71

13.08

- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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